



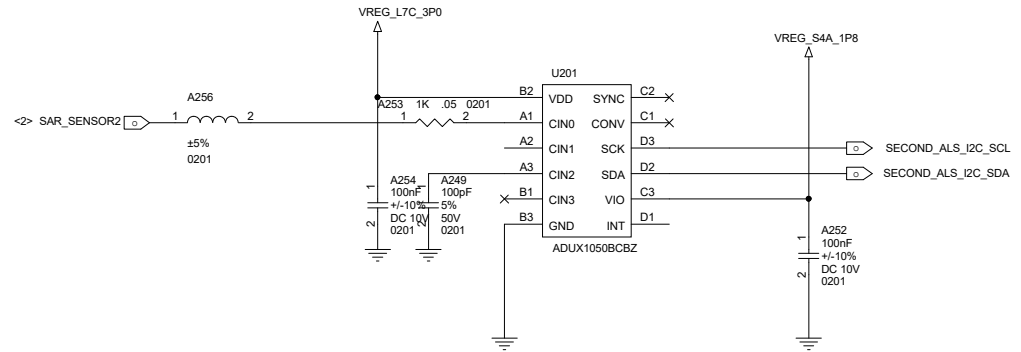
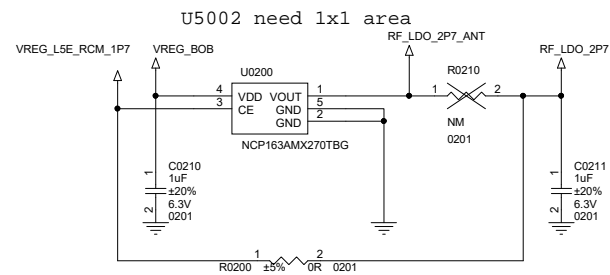
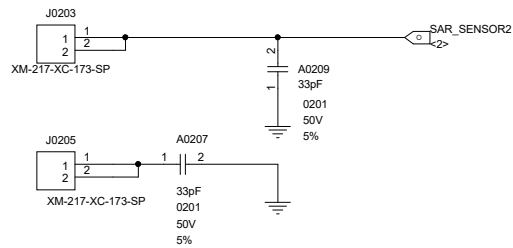
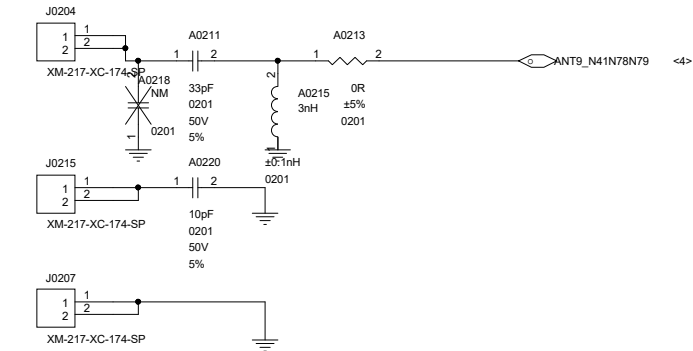
TP0102
TP0.3mm




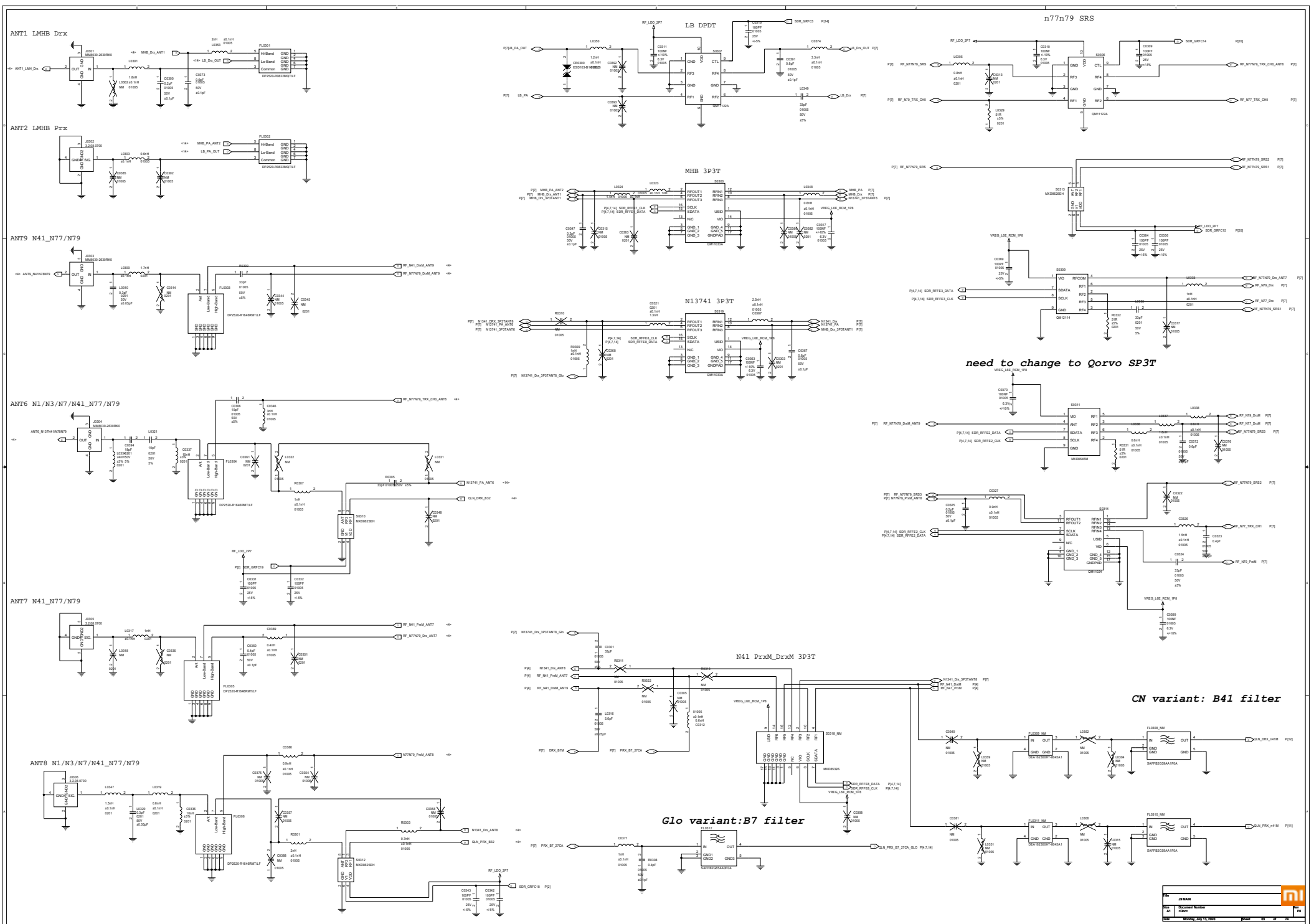
VREG_S4A_1P8

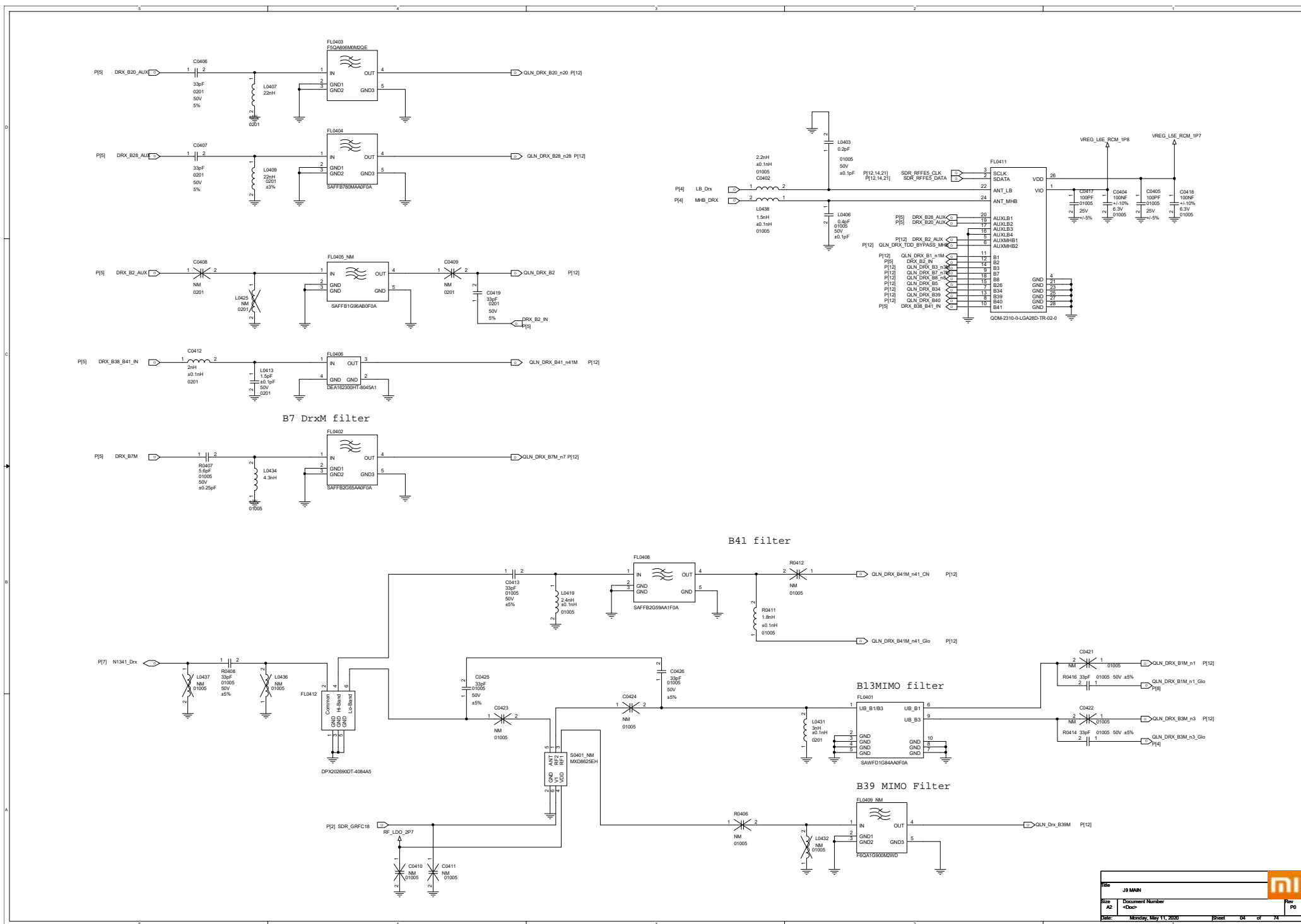


ANT9_N41N78N79

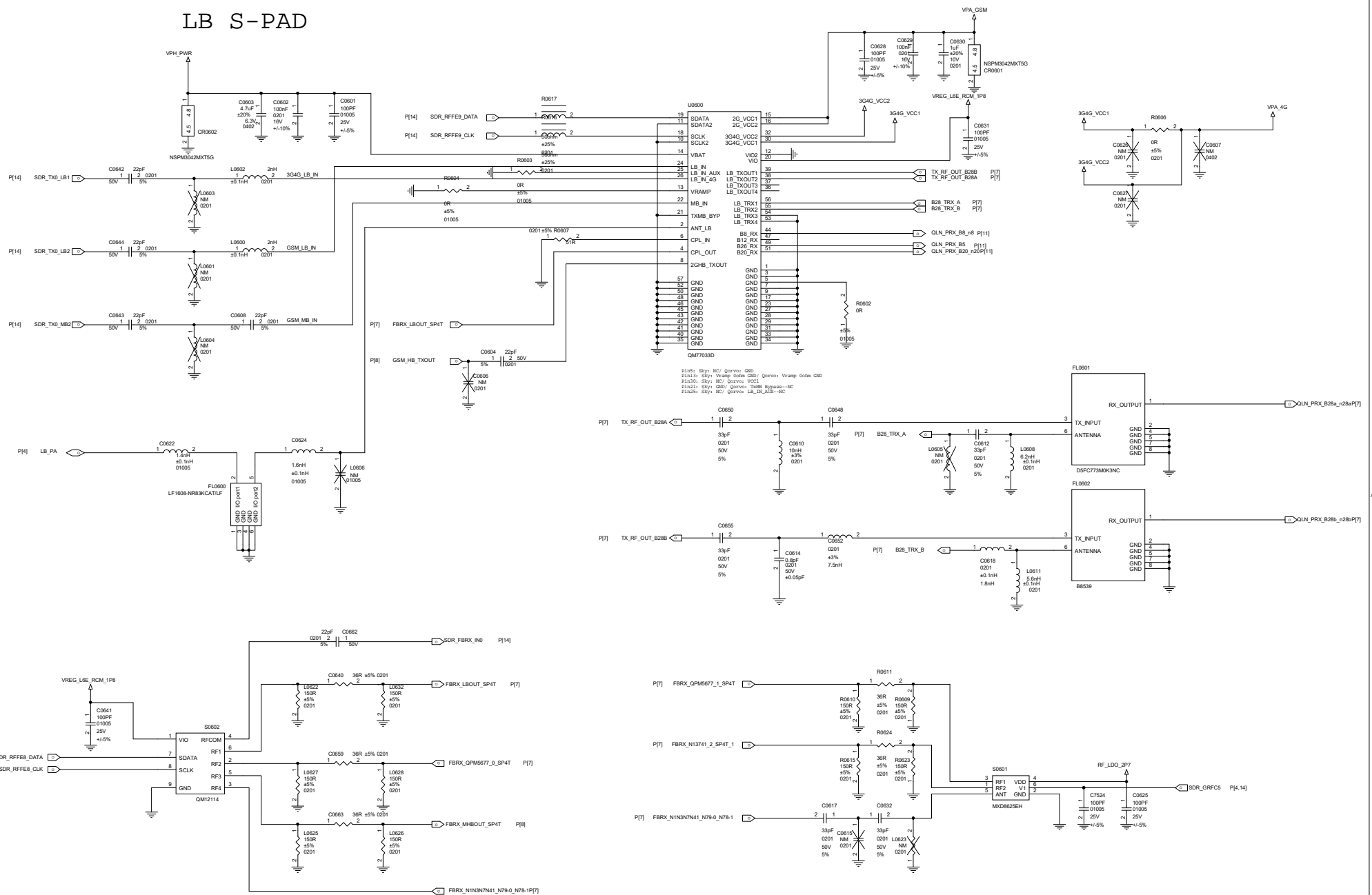



			
Title			
G7A MAIN			
Size	Document Number	Rev	
A3	<Doc>	P1B	
Date:	Tuesday, June 02, 2020	Sheet	2 of 72



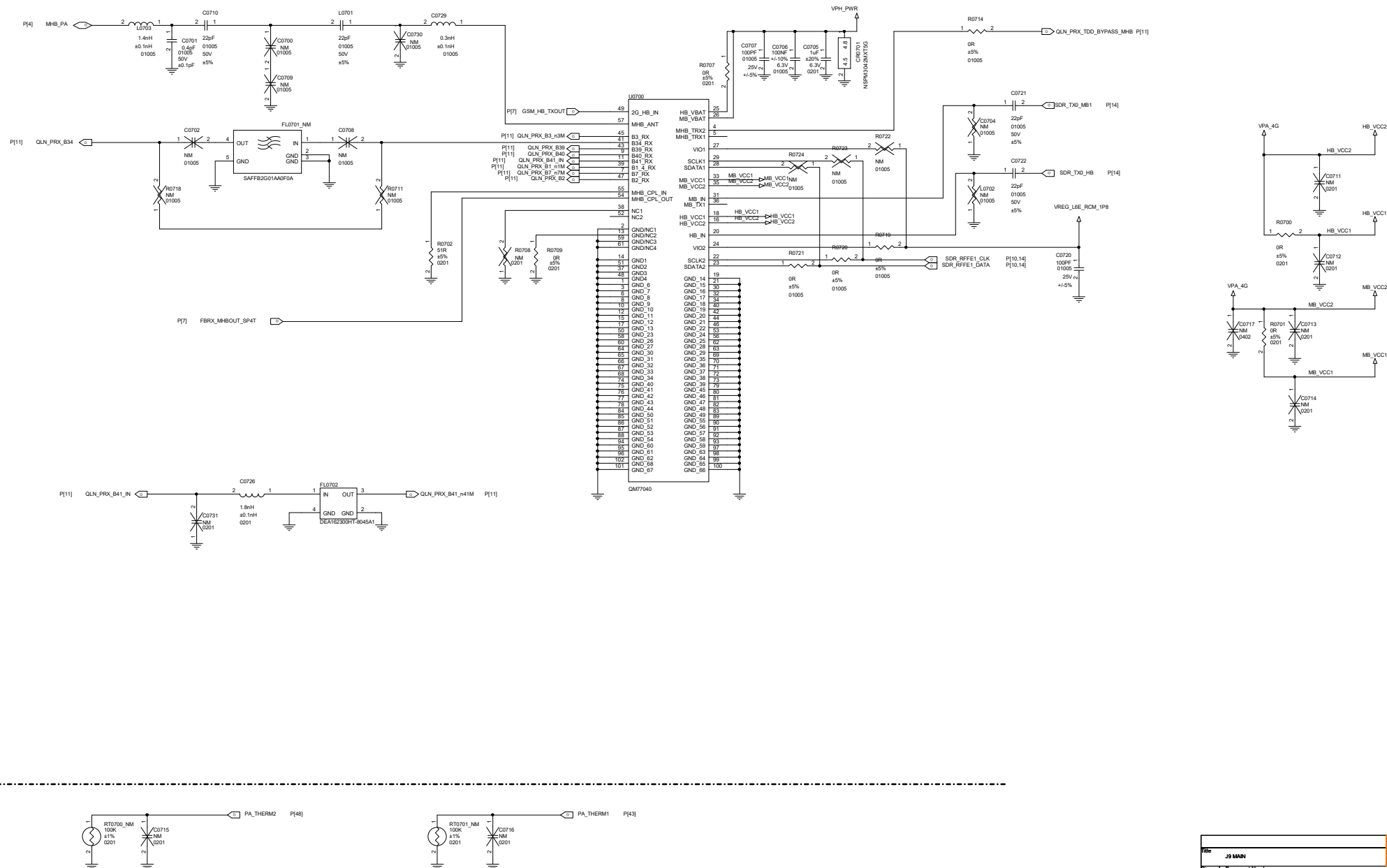


LB S-PAD

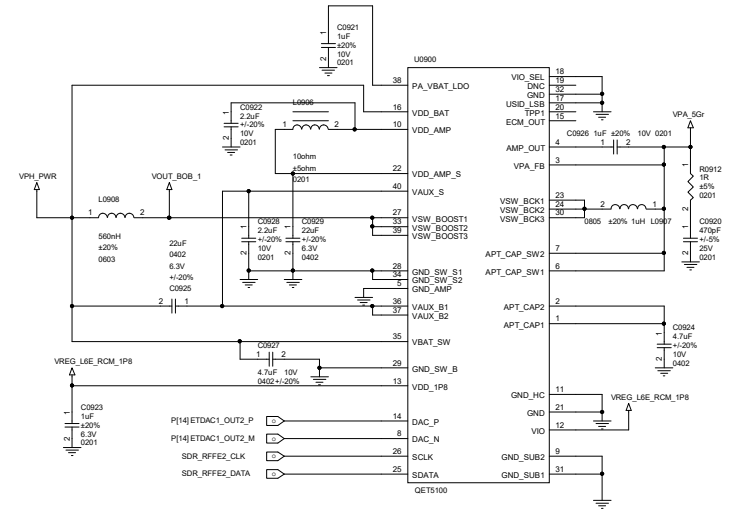
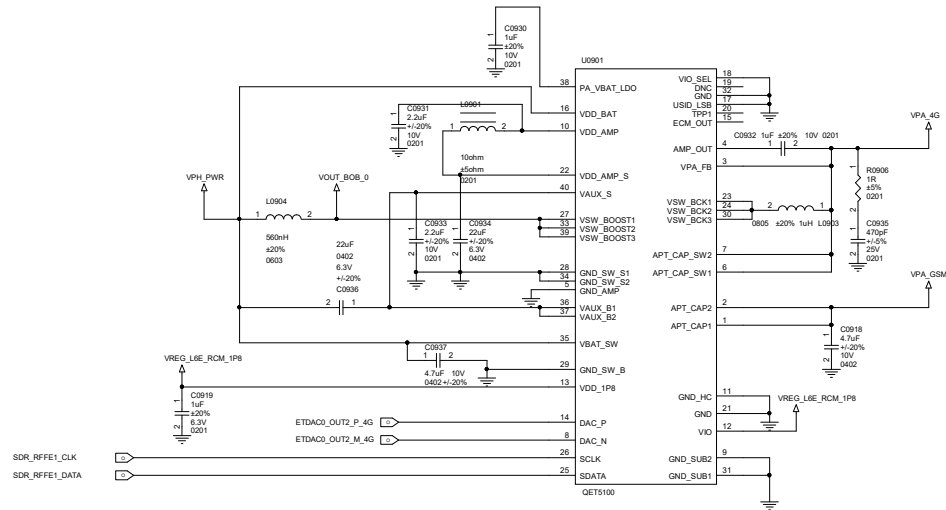


Title		J9 MAIN			
Size	Document Number				
A2	PA1			Rev	P0
Date:	Monday, June 15, 2020		Sheet	06	of 72

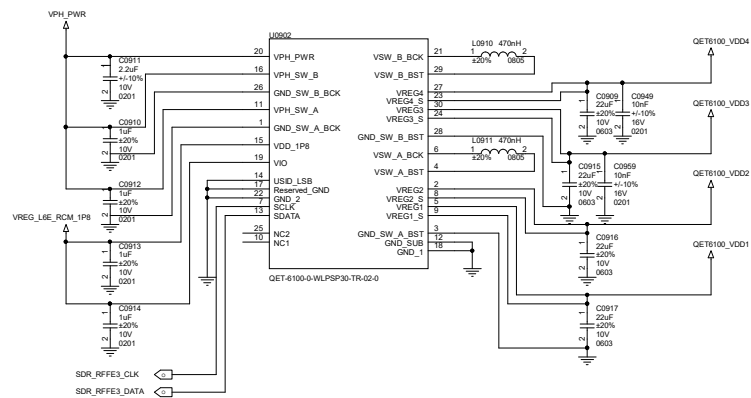
MHB S-PAD

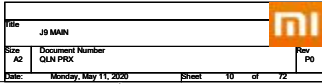


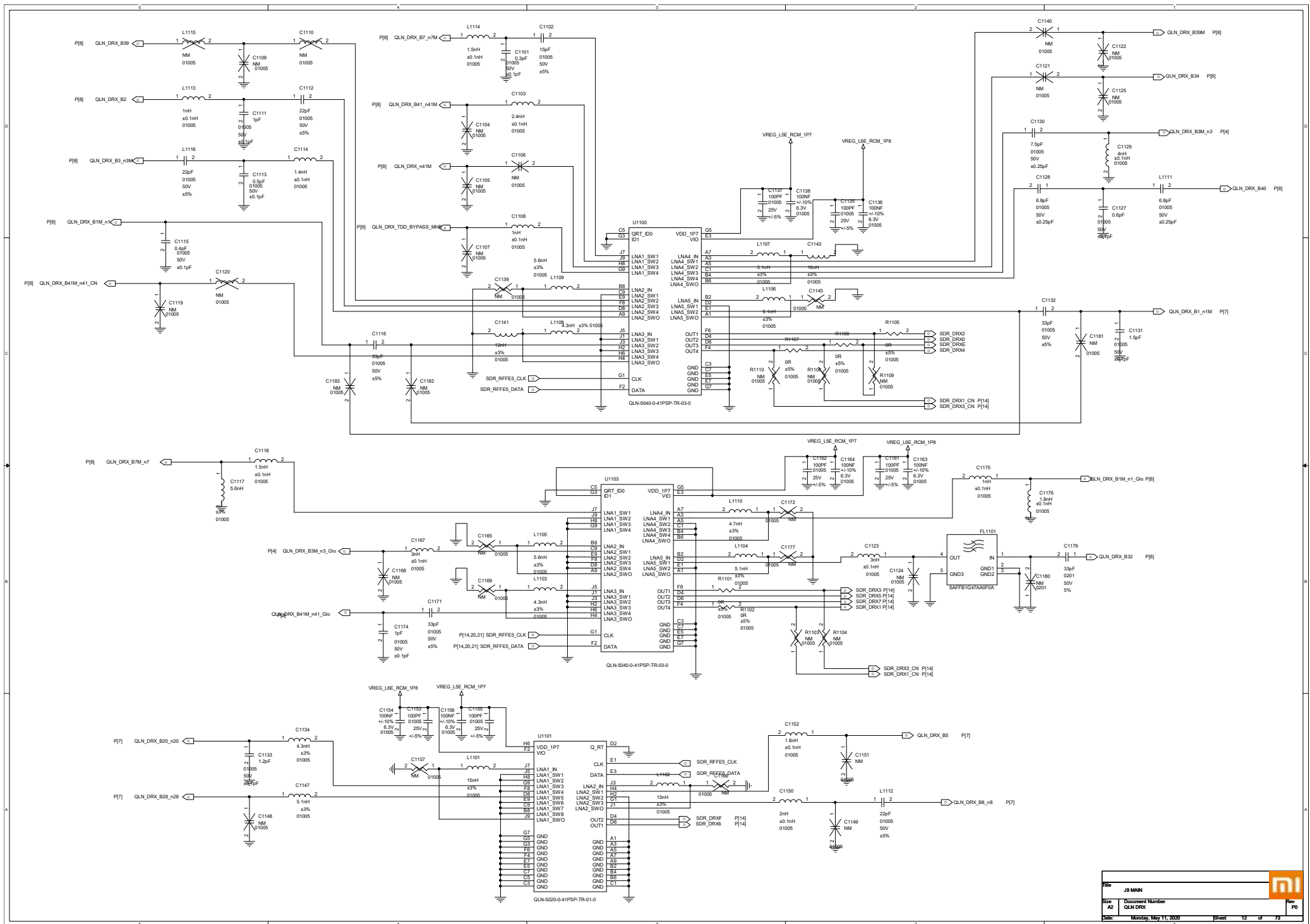
4G QET & 5G QET CH1

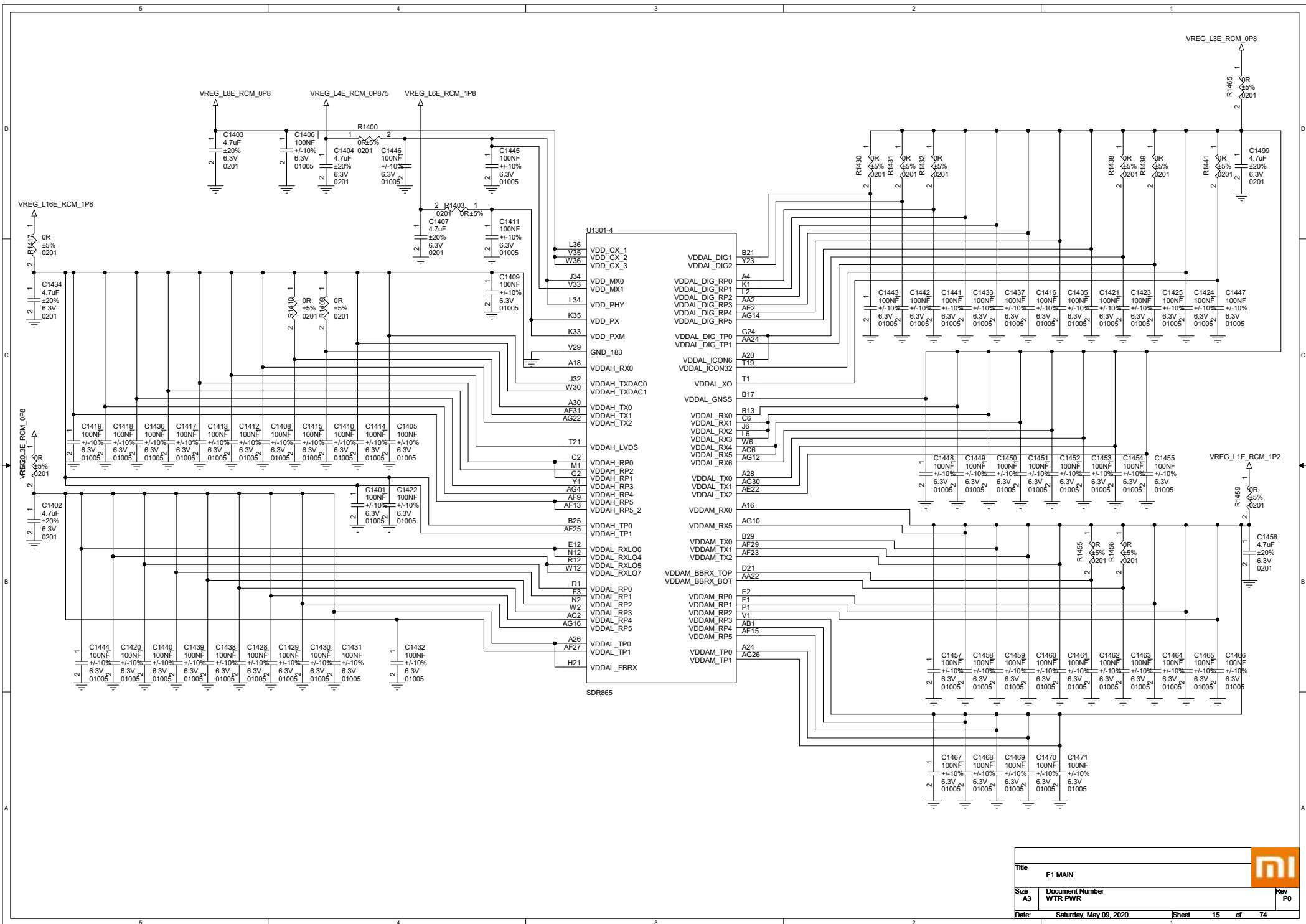


DC FOR 5G

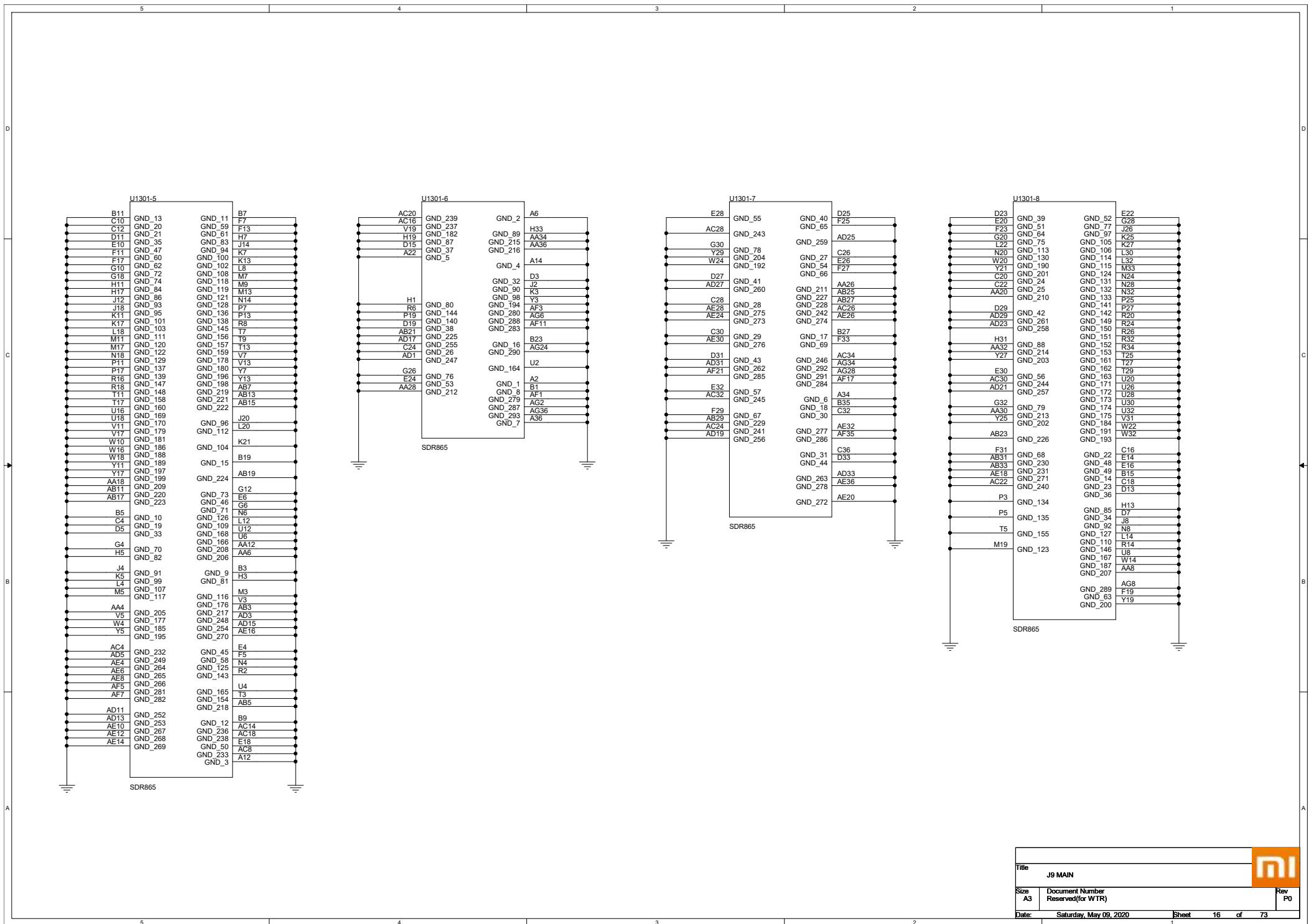


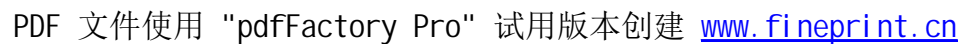


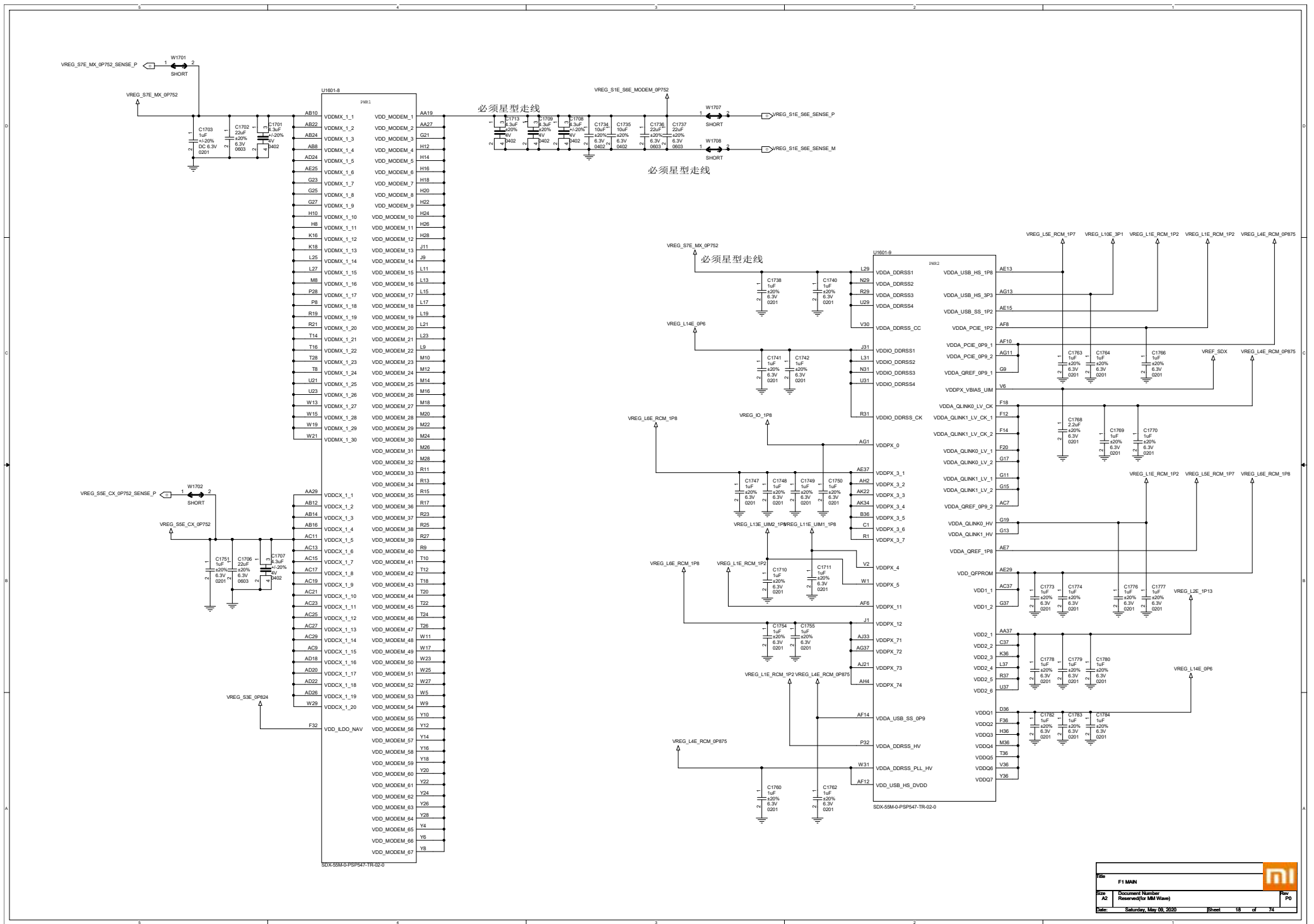


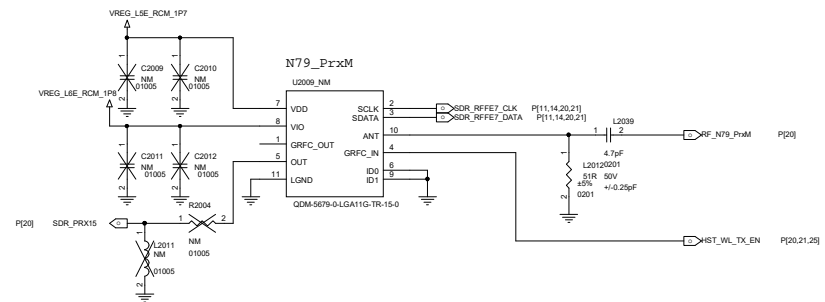
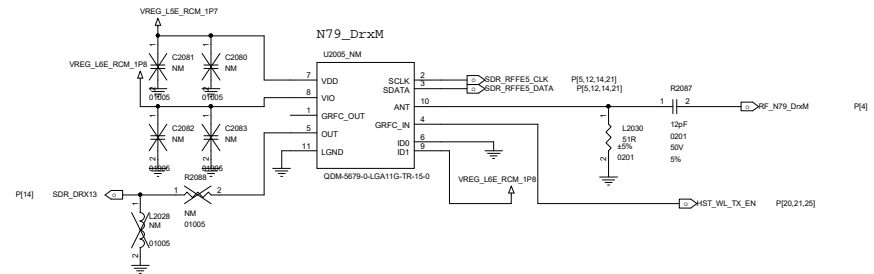
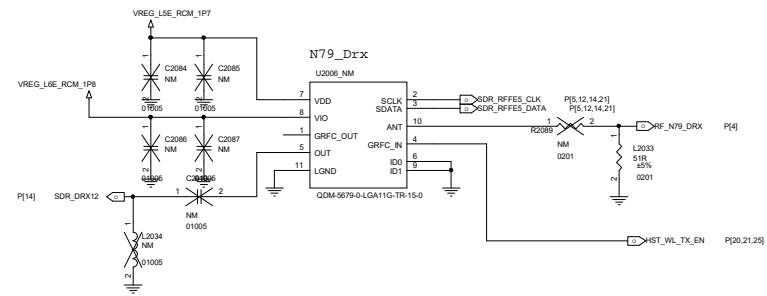


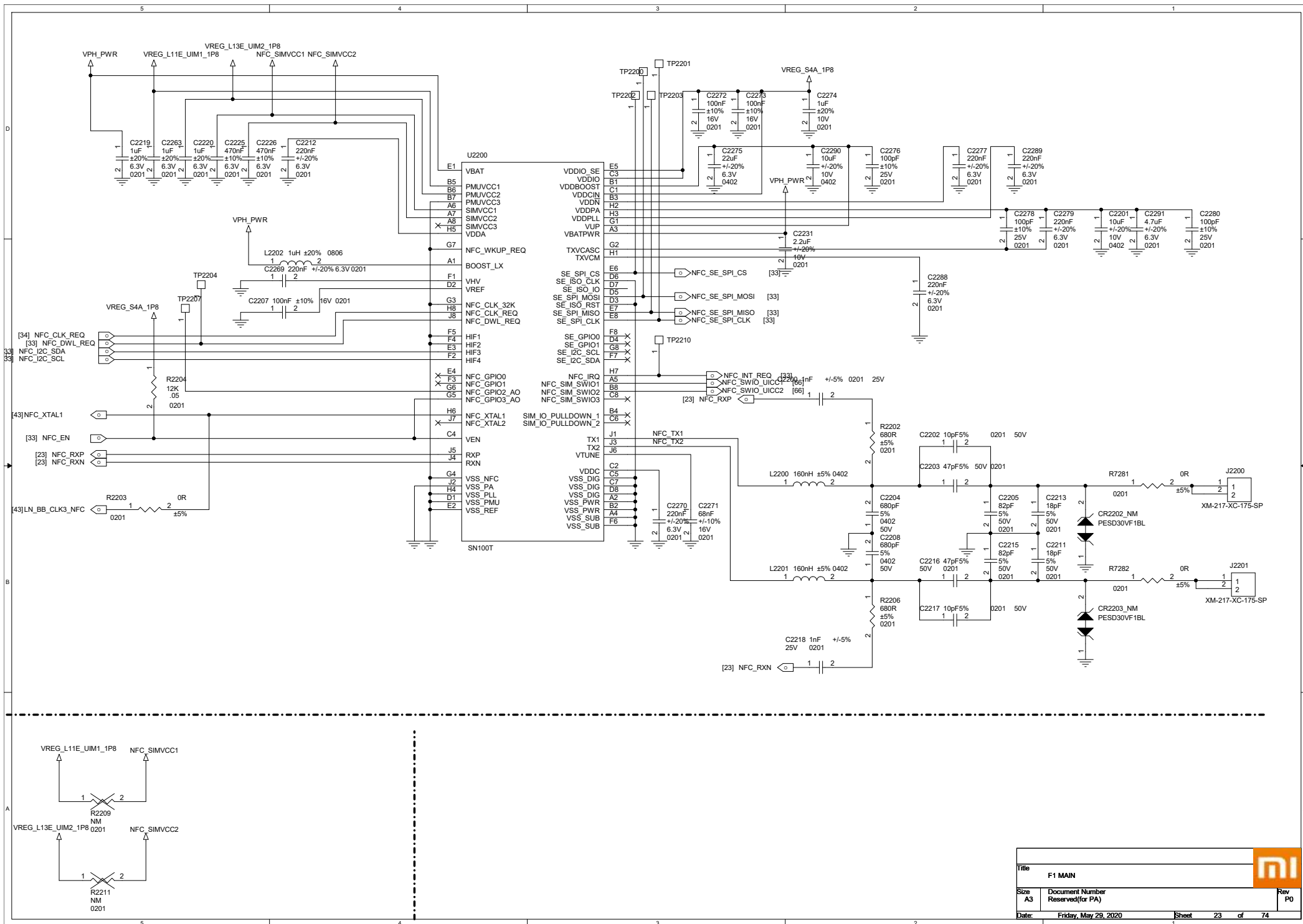
Title		F1 MAIN	Rev P0
Size	Document Number		
A3	WTR PWR		
Date:	Saturday, May 09, 2020	Sheet	15 of 74




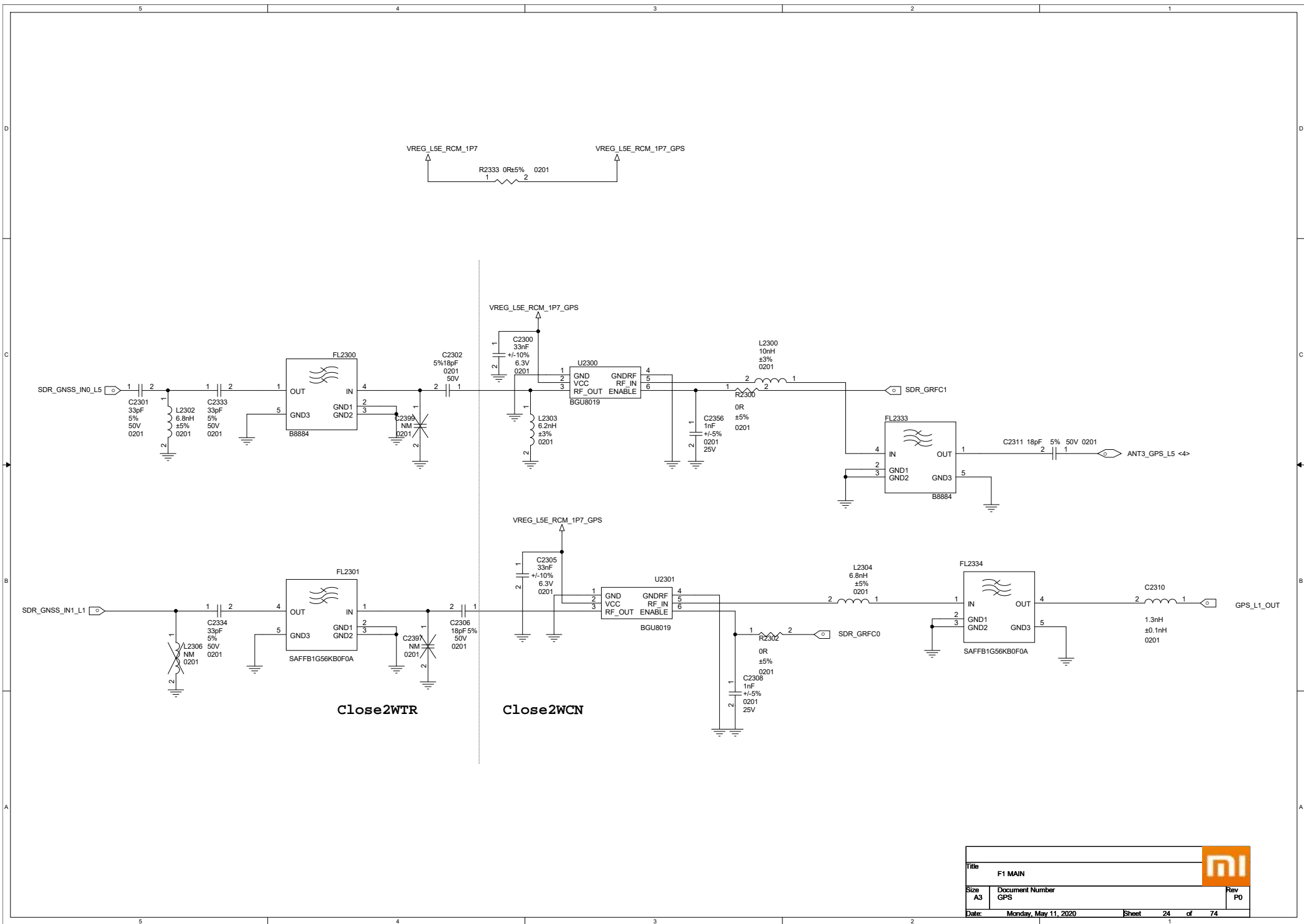


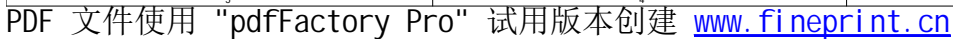


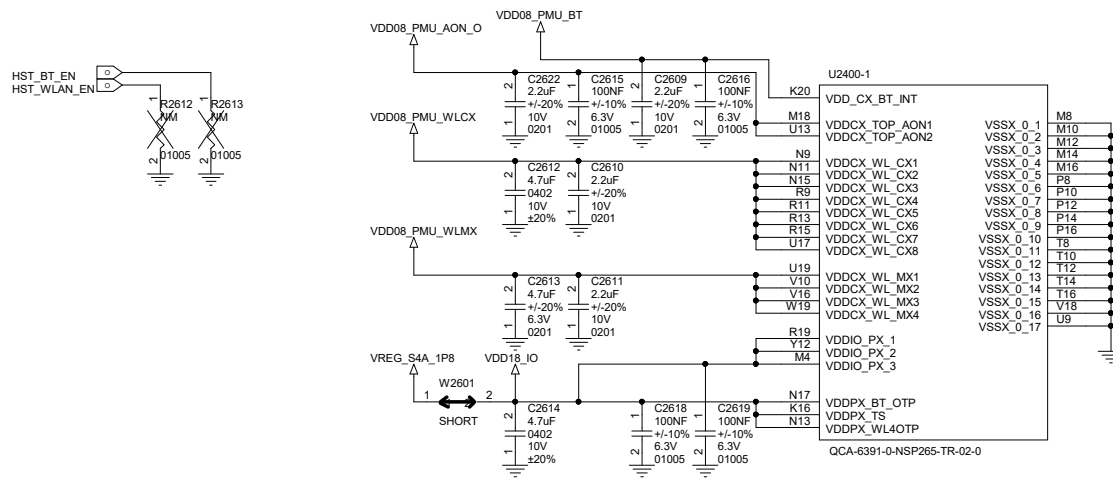
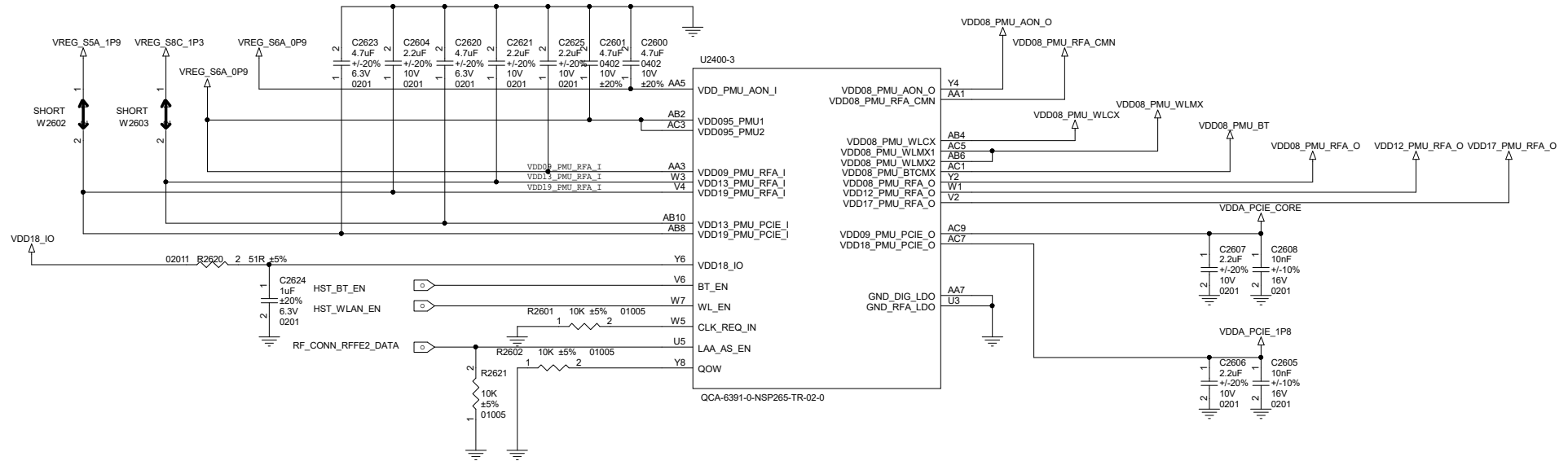




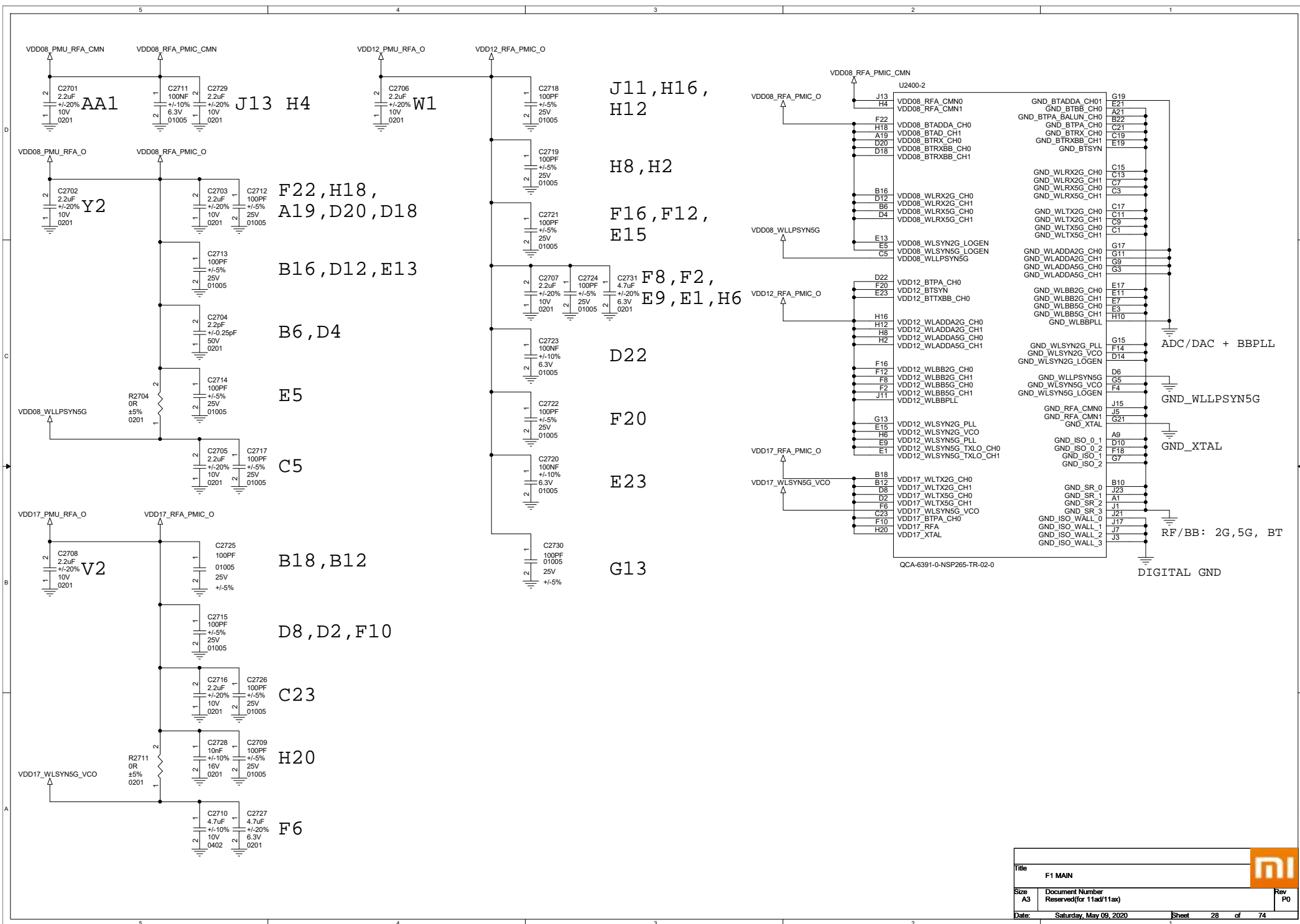
			
Title			
F1 MAIN			
Size A3	Document Number Reserved (for PA)	Rev P0	
Date:	Friday, May 29, 2020	Sheet 23 of 74	






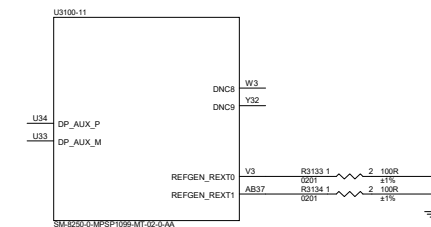


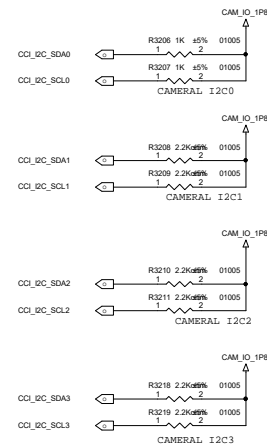
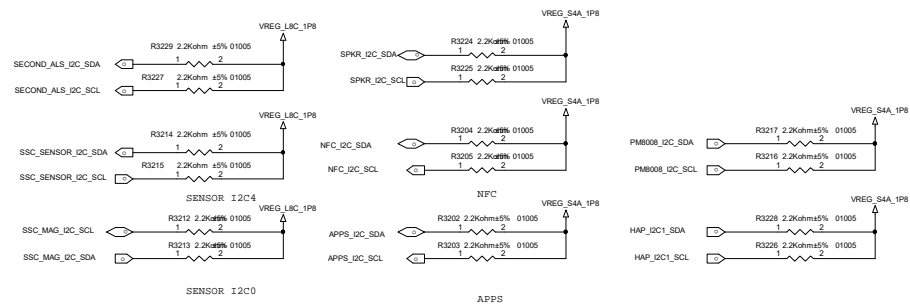
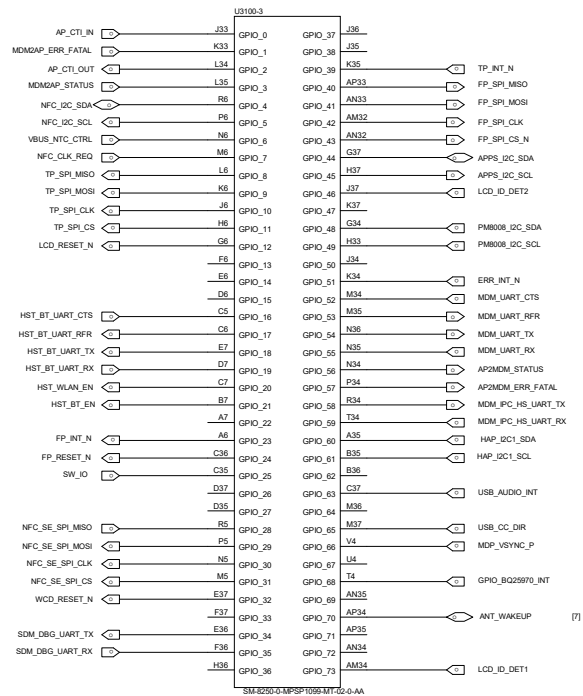
Title		F1 MAIN	
Size	A3	Document Number	WIFI Decoupling
Date:	Saturday, May 09, 2020	Sheet	27 of 74

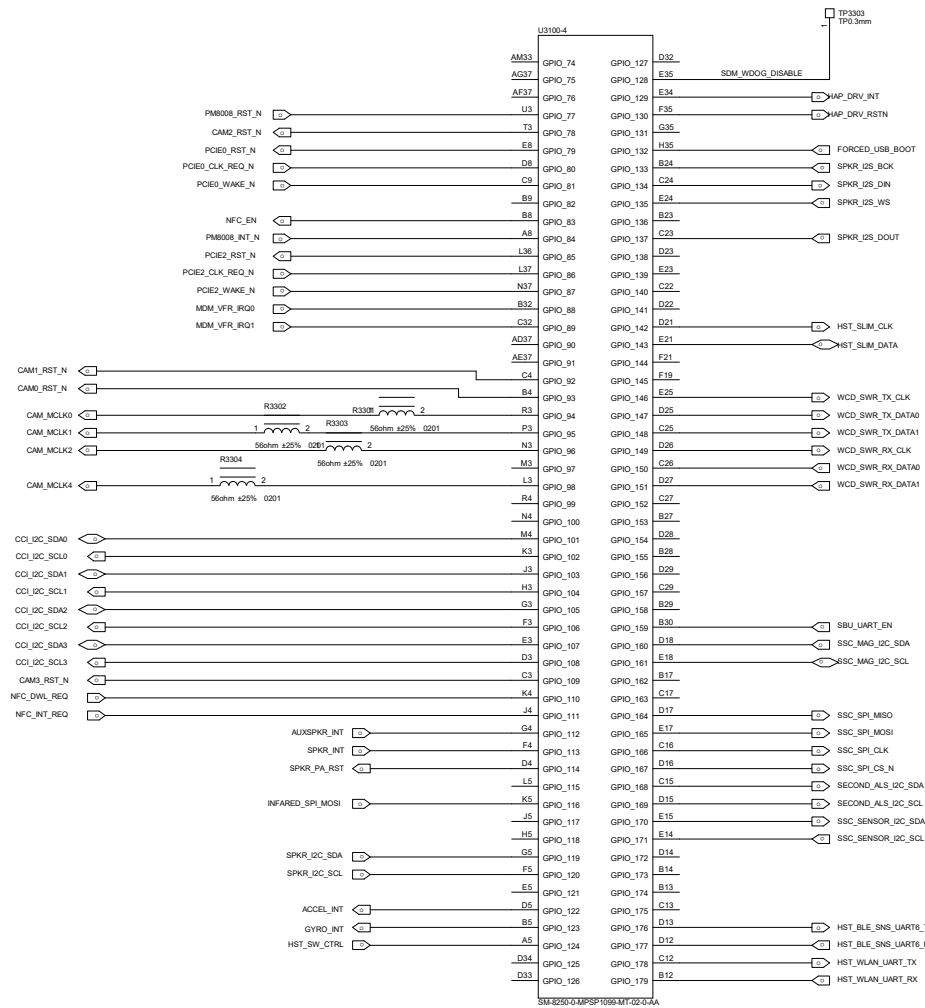


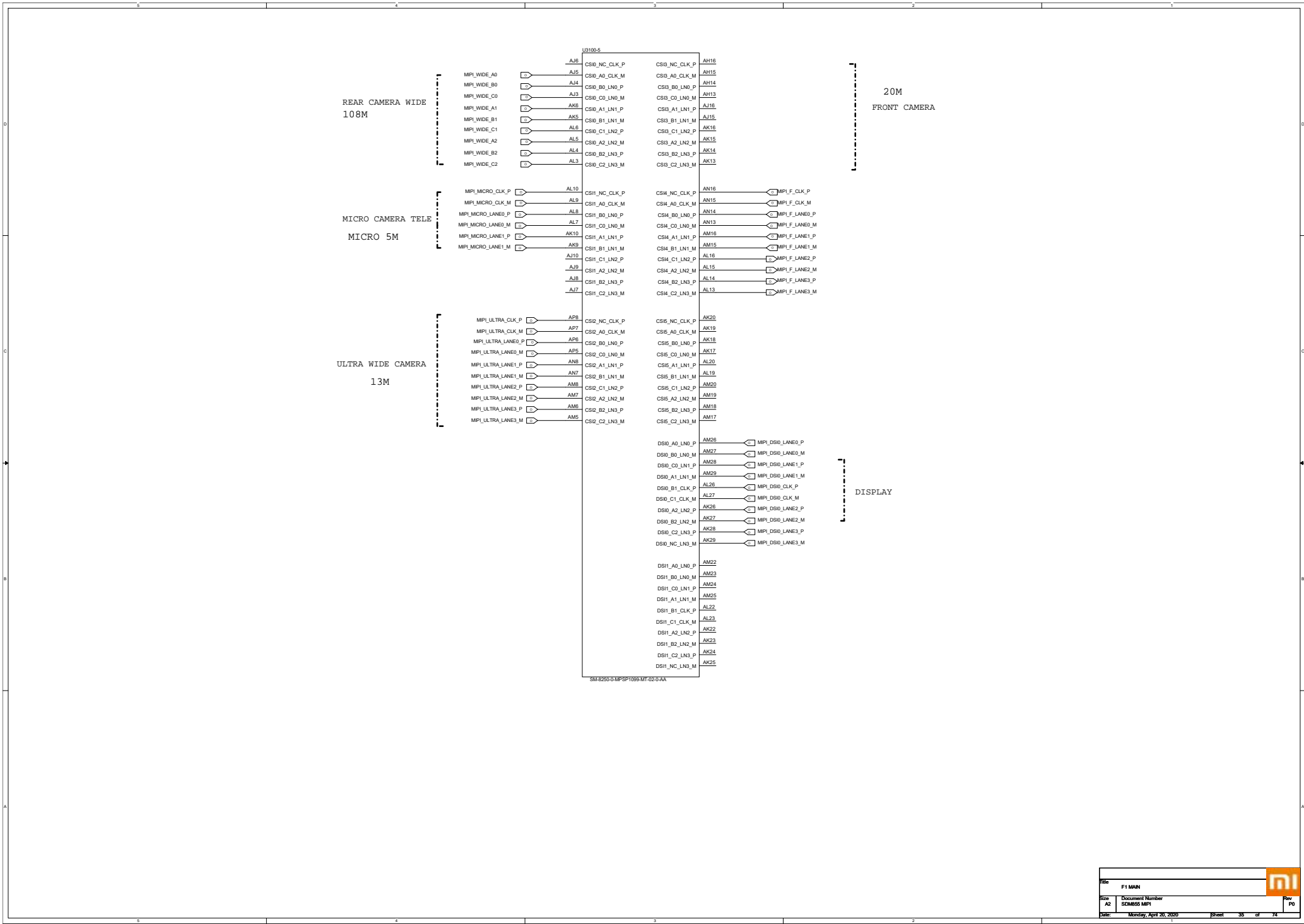
				
Title				
F1 MAIN				
Size	Document Number			Rev
A3	Reserved(for 11ad/11ax)			
Date	Saturday, May 09, 2020		Sheet	28 of 74

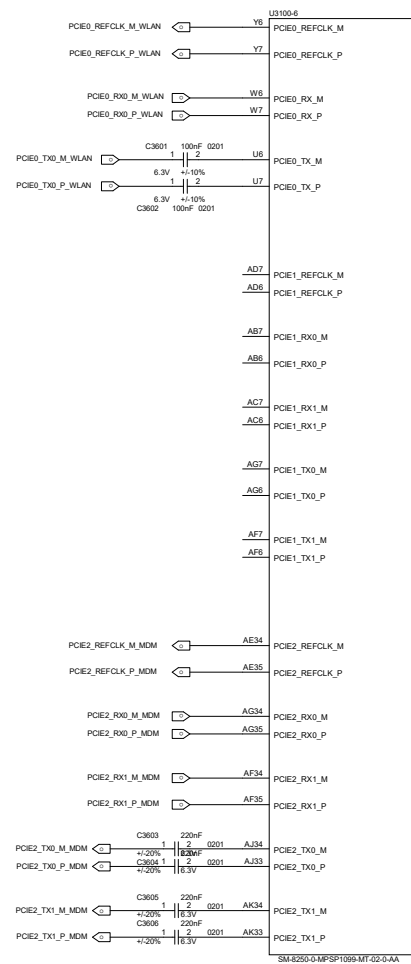
SM8250 LP4: Mount R3129/R3130/R3131/R3132

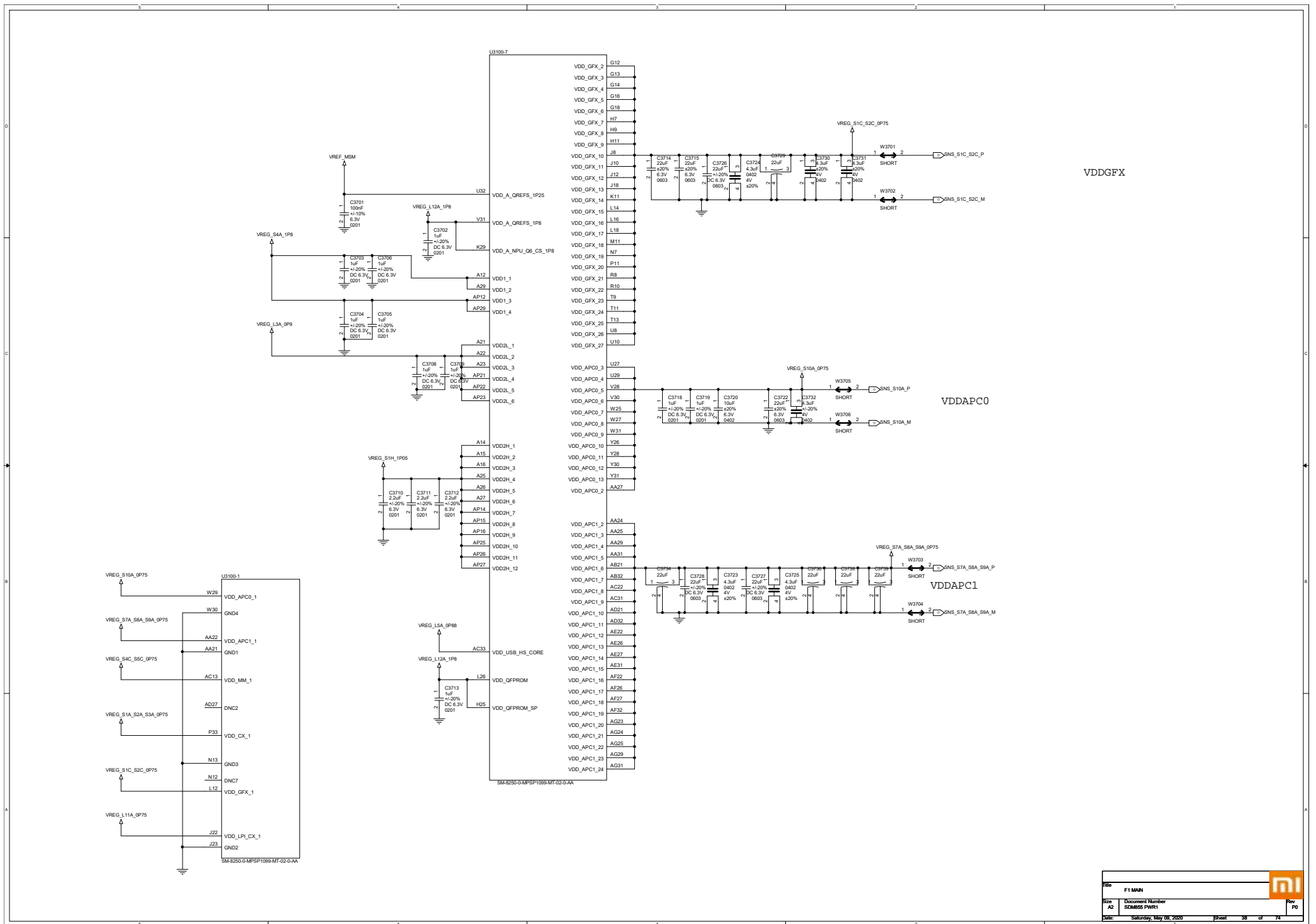


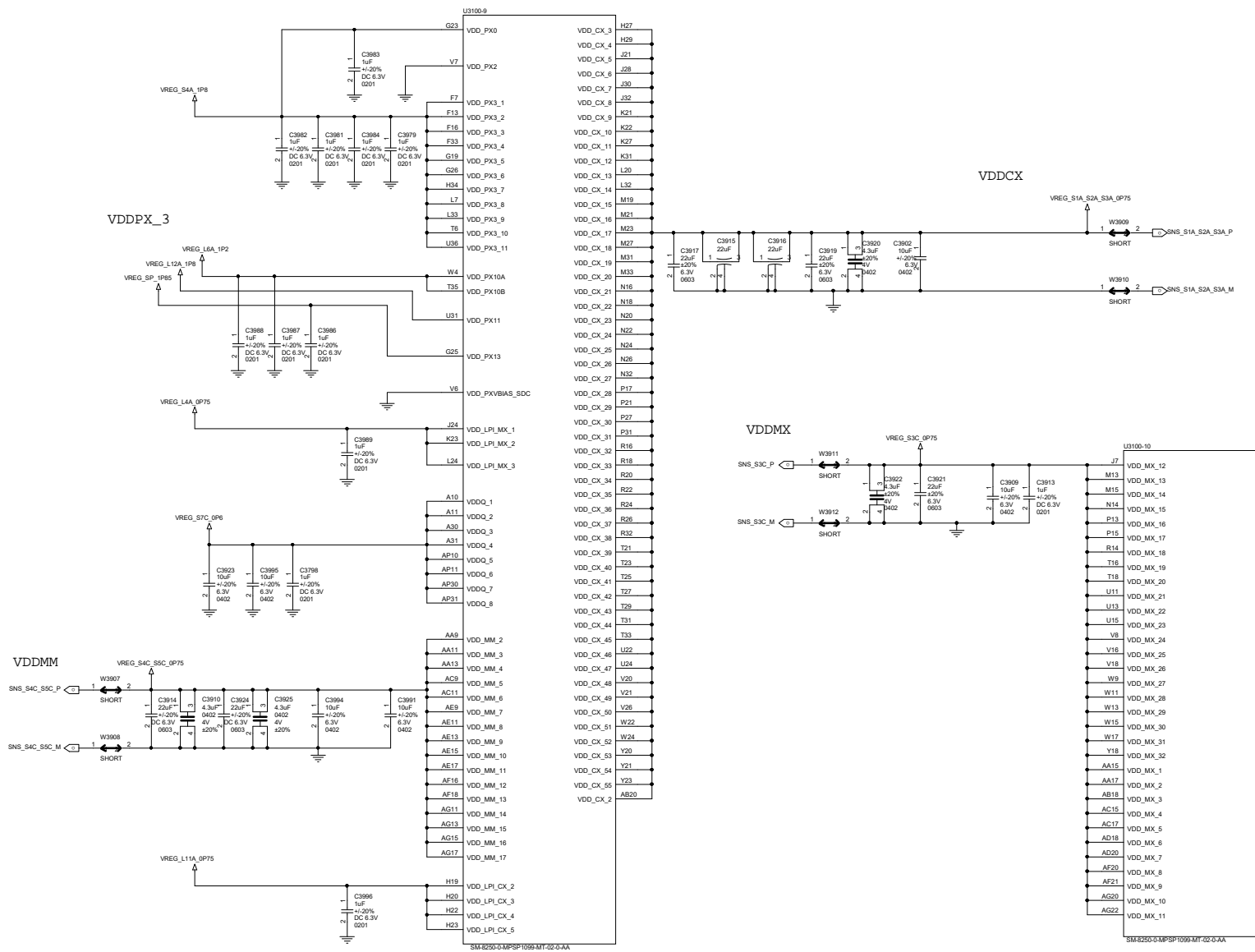


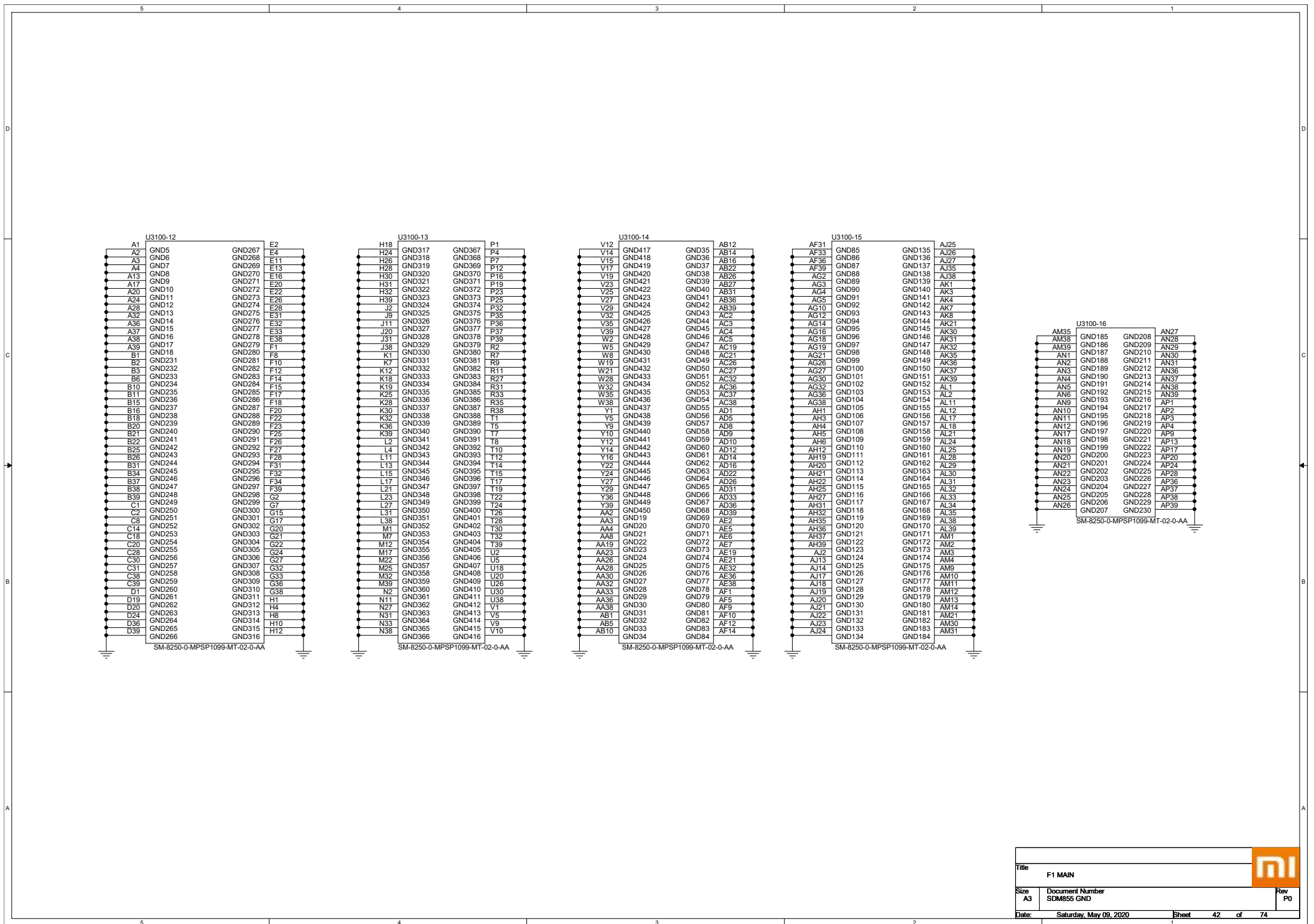











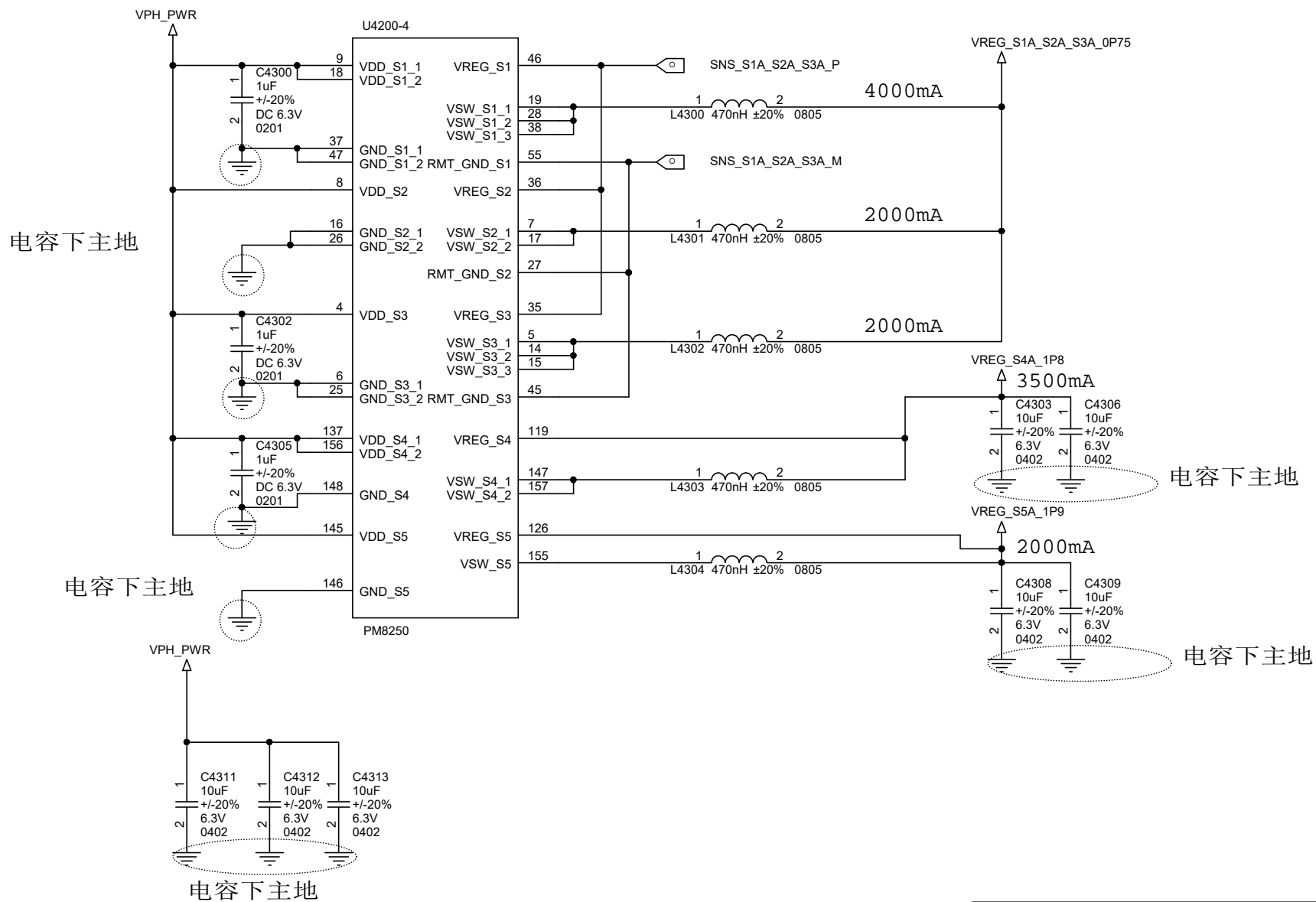



Layout Note:

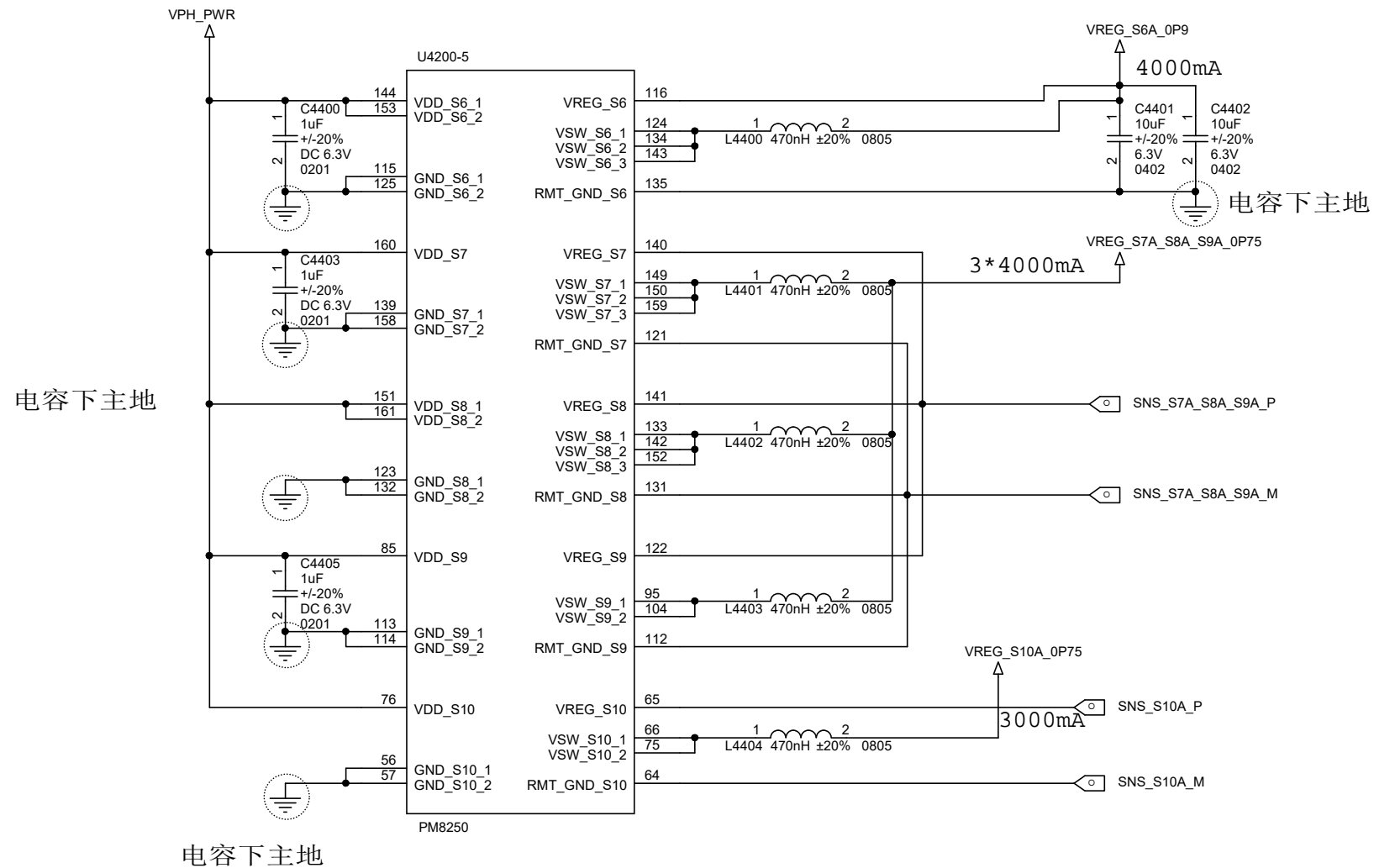
[illegible]


				
Title F1 MAIN				
Size A4	Document Number PM855 XO&CTRL			Rev PC
Date:	Monday, June 15, 2020		Sheet 43 of 74	

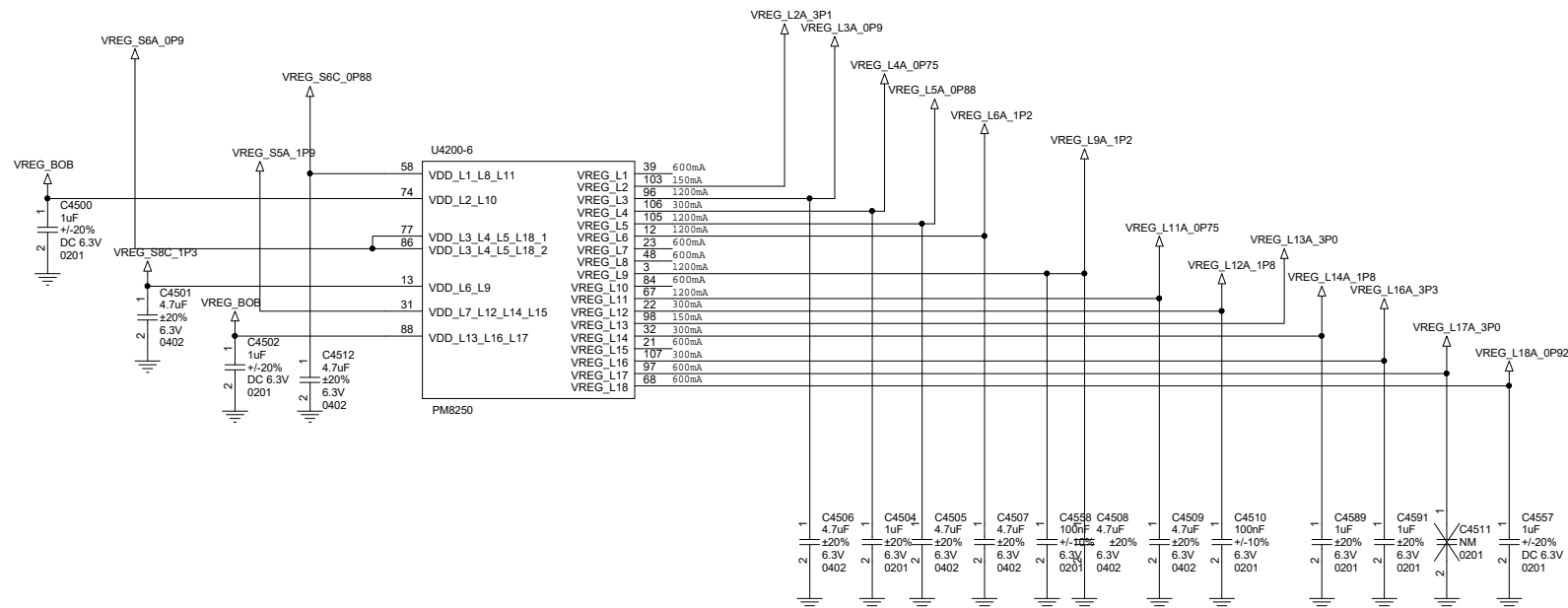





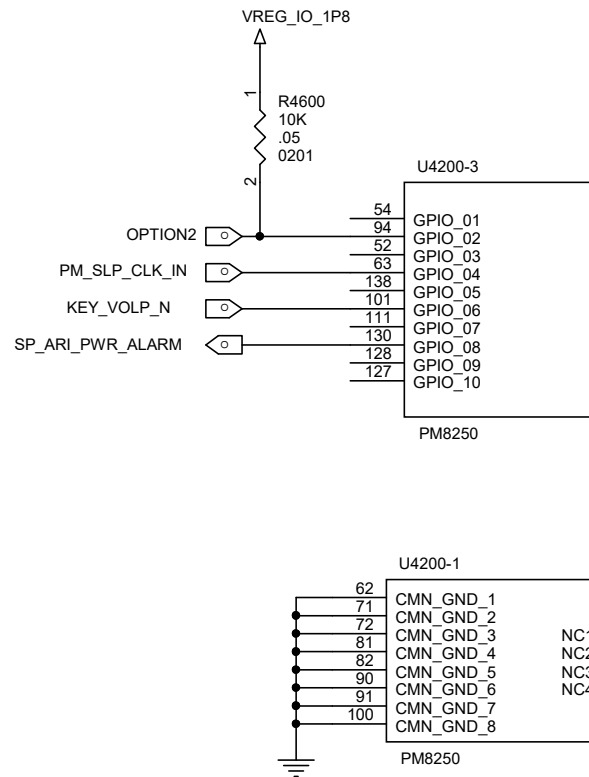
				
Title F1 MAIN				
Size A4	Document Number PM855 BUCK CONVERTER1			Rev P0
Date:	Saturday, May 09, 2020	Sheet	44 of 74	




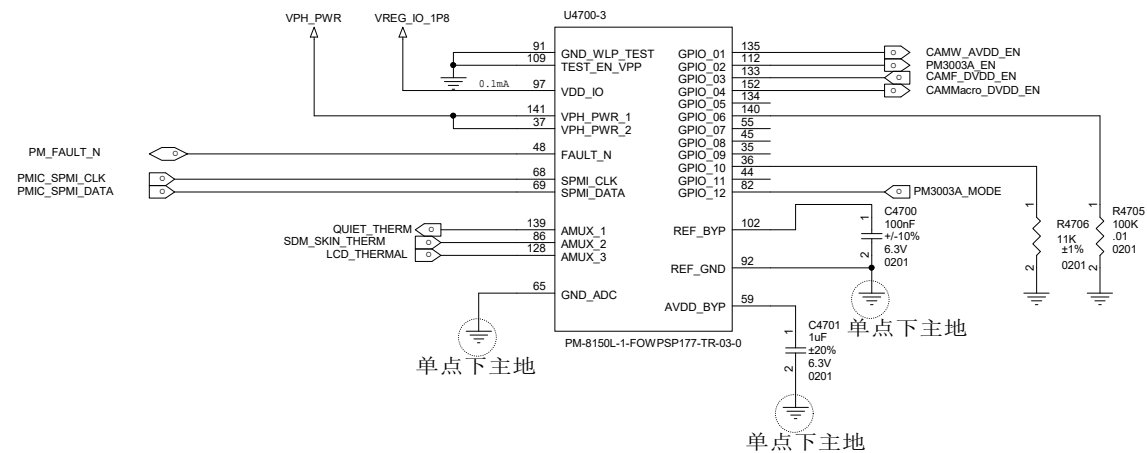
				
Title F1 MAIN				
Size A4	Document Number PM855 BUCK CONVERTER2			Rev P0
Date:	Saturday, May 09, 2020		Sheet 45 of 74	



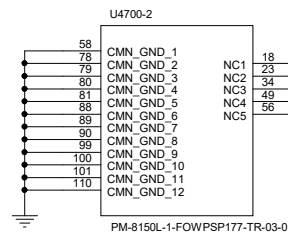
			
Title			
F-1 MAIN			
Size	Document Number		
A3	PM855 LDOs		Rev
			P0
Date:	Saturday, May 09, 2020	Sheet	46 of 74




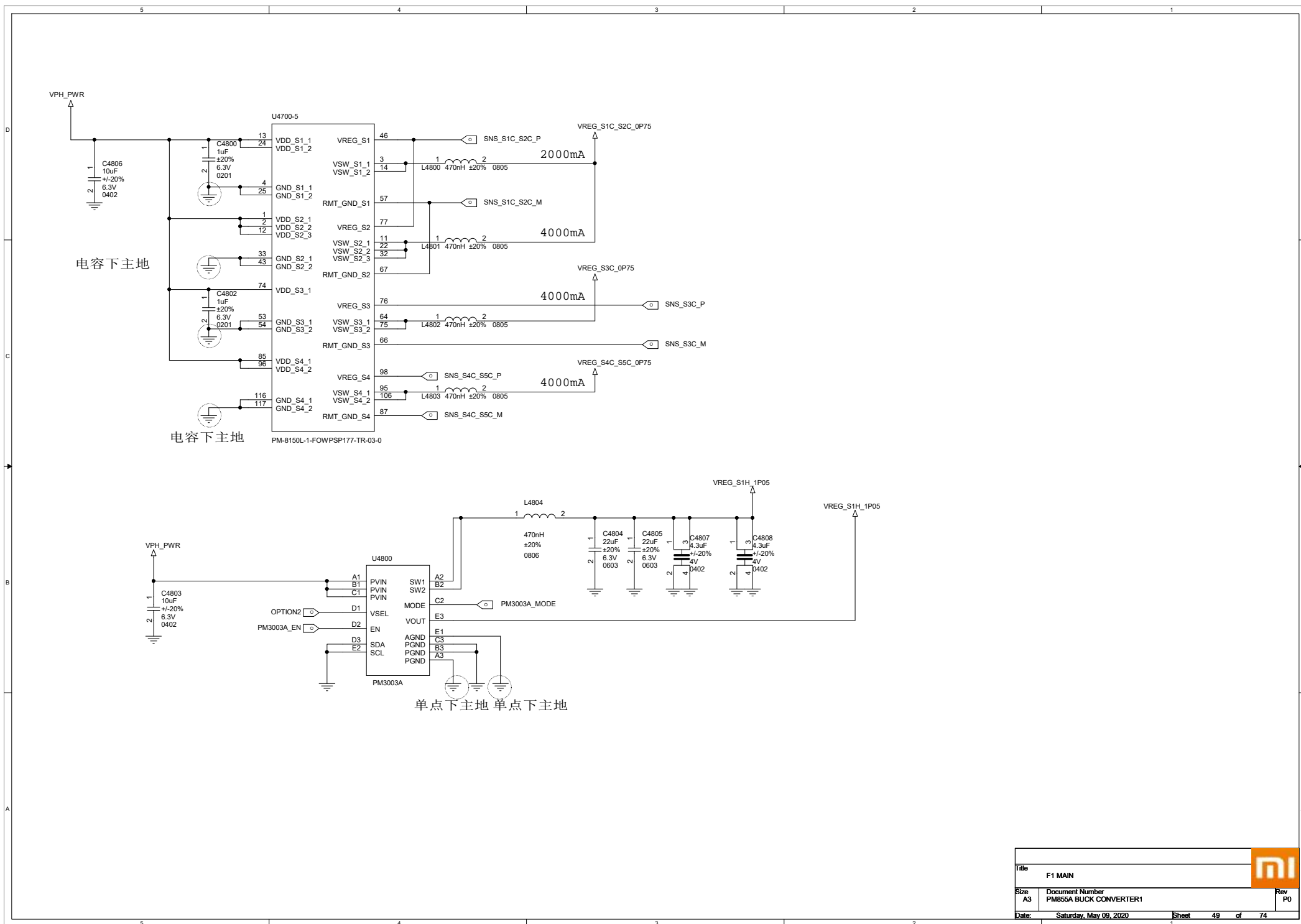
				
Title F1 MAIN				
Size A4	Document Number PM855 GPIO&GND			Rev P0
Date:	Saturday, May 09, 2020		Sheet 47 of 74	




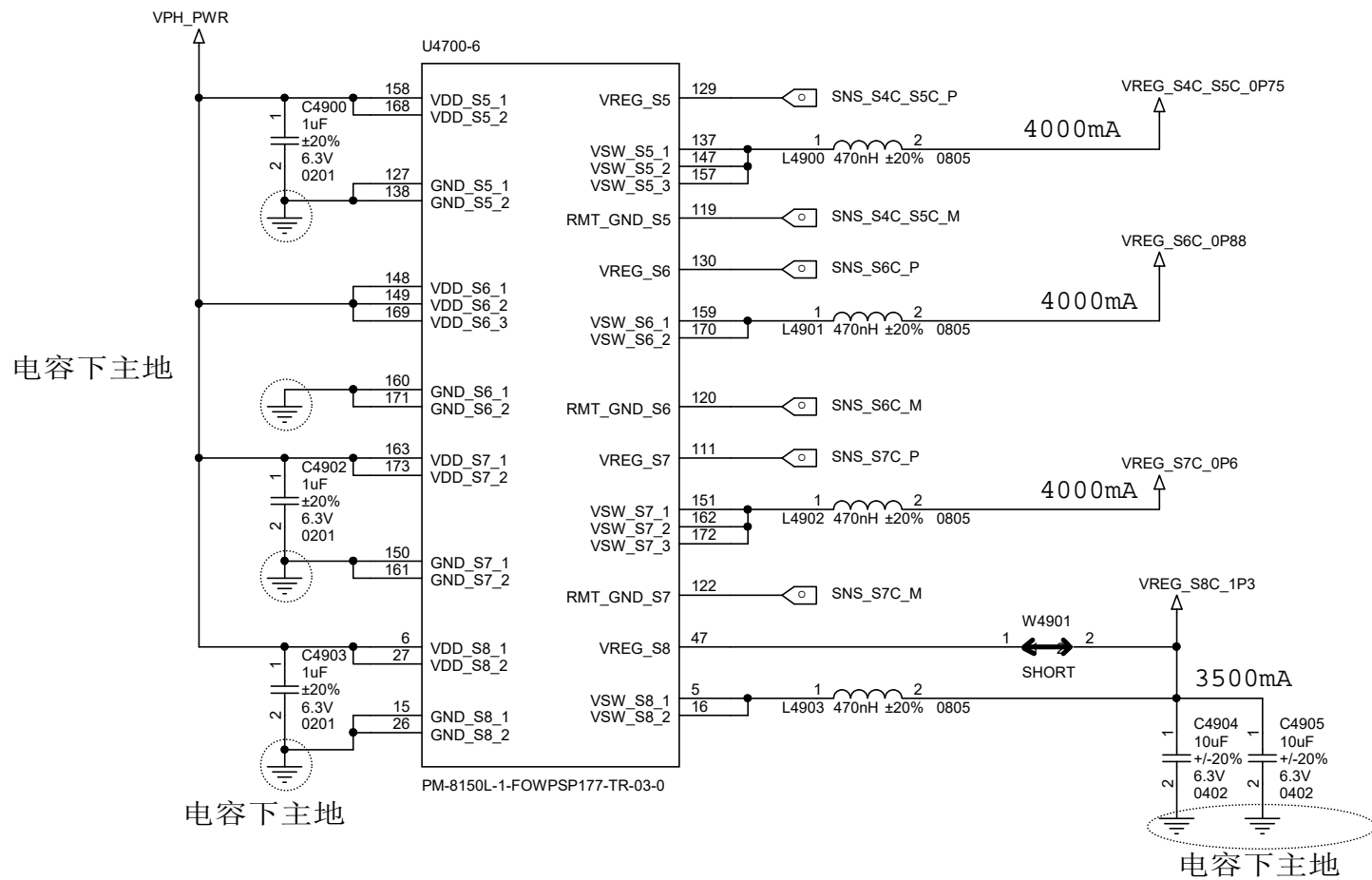
R4705: F1 Project ID
 R4706: Hardware ID:
 P0: 17.8K
 P1: 24.9K
 P2: 33.2K




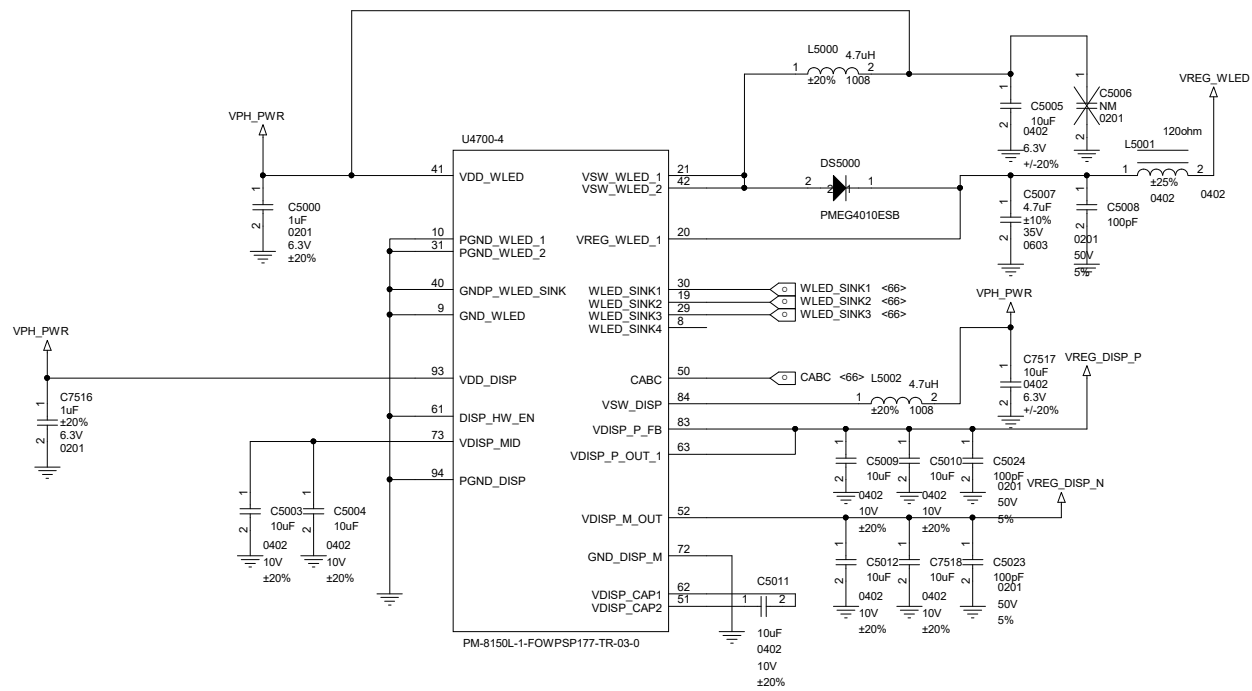
			
Title			
F1 MAIN			
Size	Document Number	Rev	
A3	PM855A CTRL&GND	P0	
Date:	Monday, July 13, 2020	Sheet	48 of 74



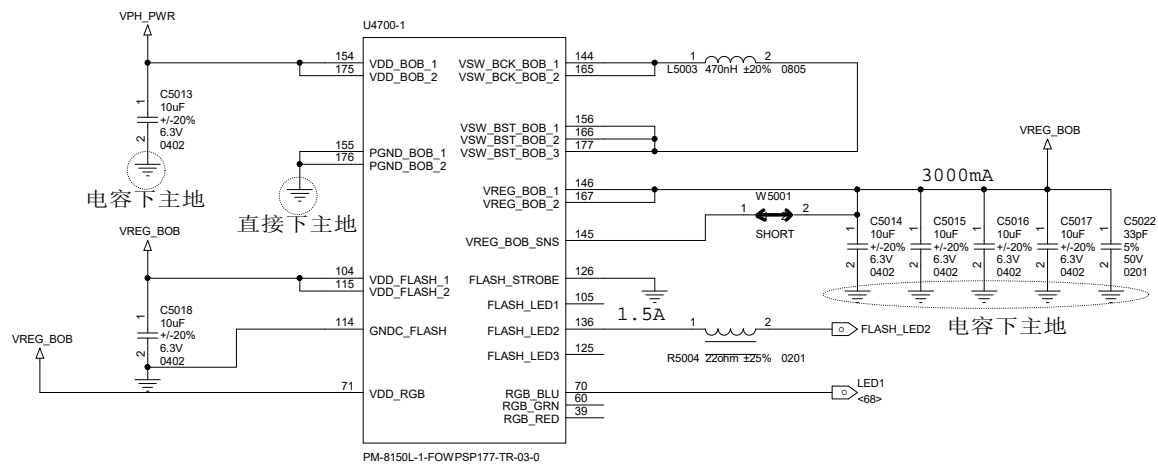
				
Title				
F1 MAIN				
Size	Document Number			
A3	PM855A BUCK CONVERTER1			Rev
Date:		Saturday, May 09, 2020	Sheet	49 of 74
				P0




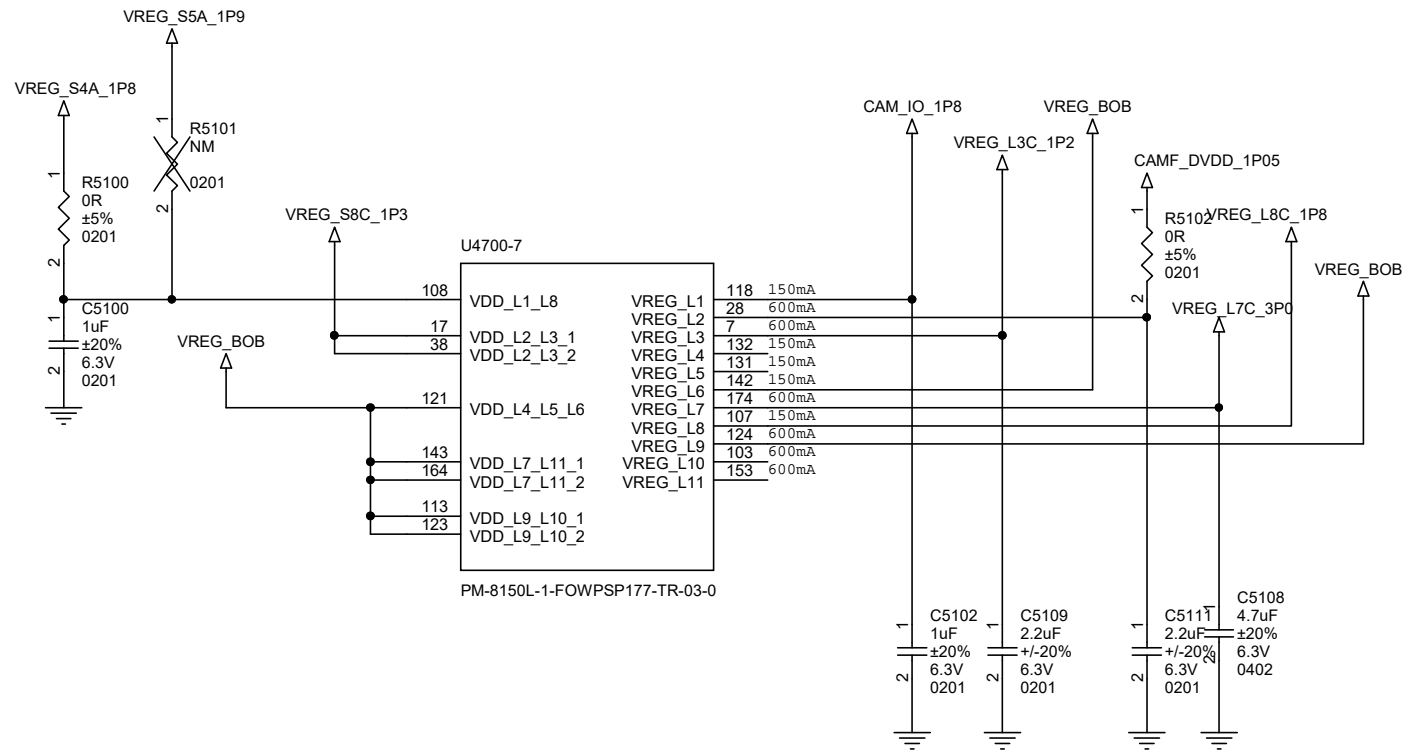
				
Title F1 MAIN				
Size A4	Document Number PM855A BUCK CONVERTER2			Rev P0
Date:	Saturday, May 09, 2020		Sheet 49 of 74	




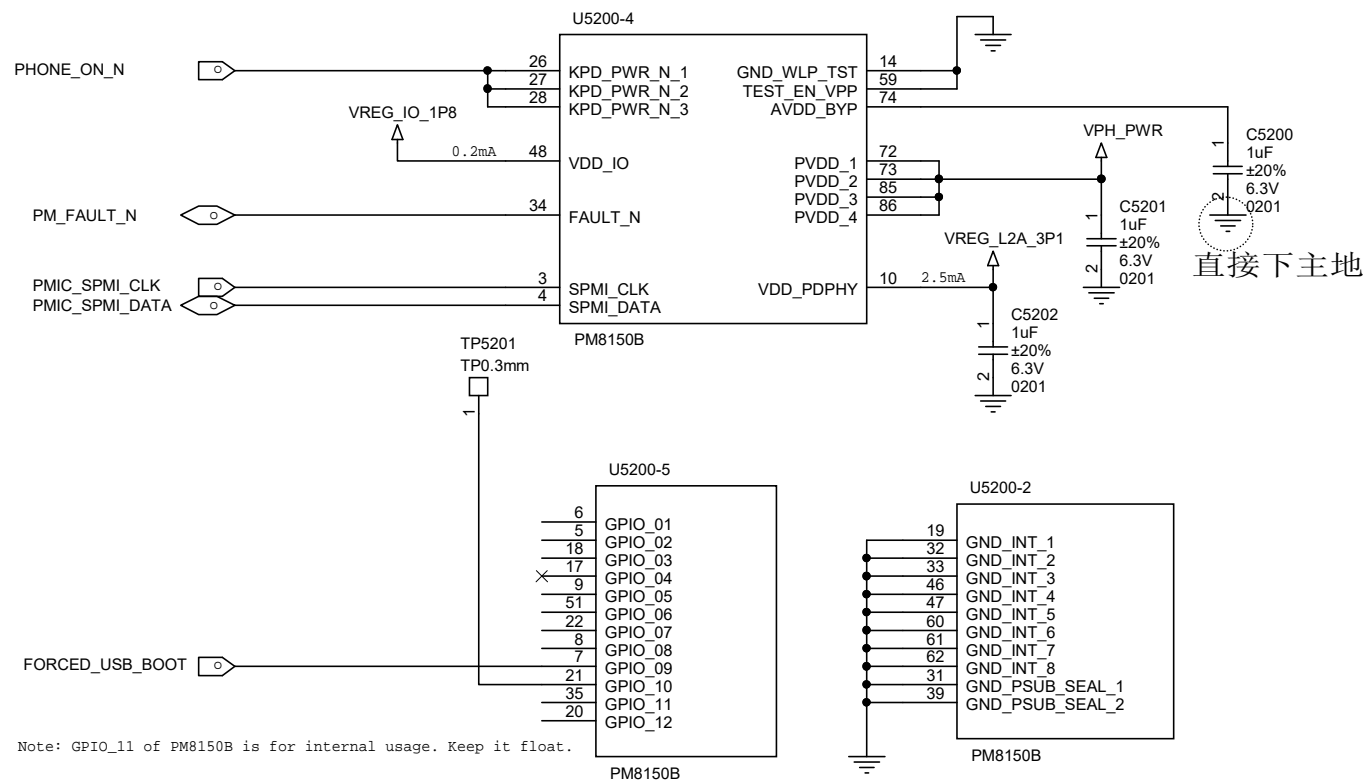
C5011 SHOULD BE CLOSE TO PINS AS POSSIBLE



			
Title F1 MAIN			
Size A3	Document Number PM855A AMOLED&BOB&FLASH		Rev P0
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Title F1 MAIN				
Size A4	Document Number PM855A LDOs			Rev P0
Date:	Saturday, May 09, 2020		Sheet 51 of 74	

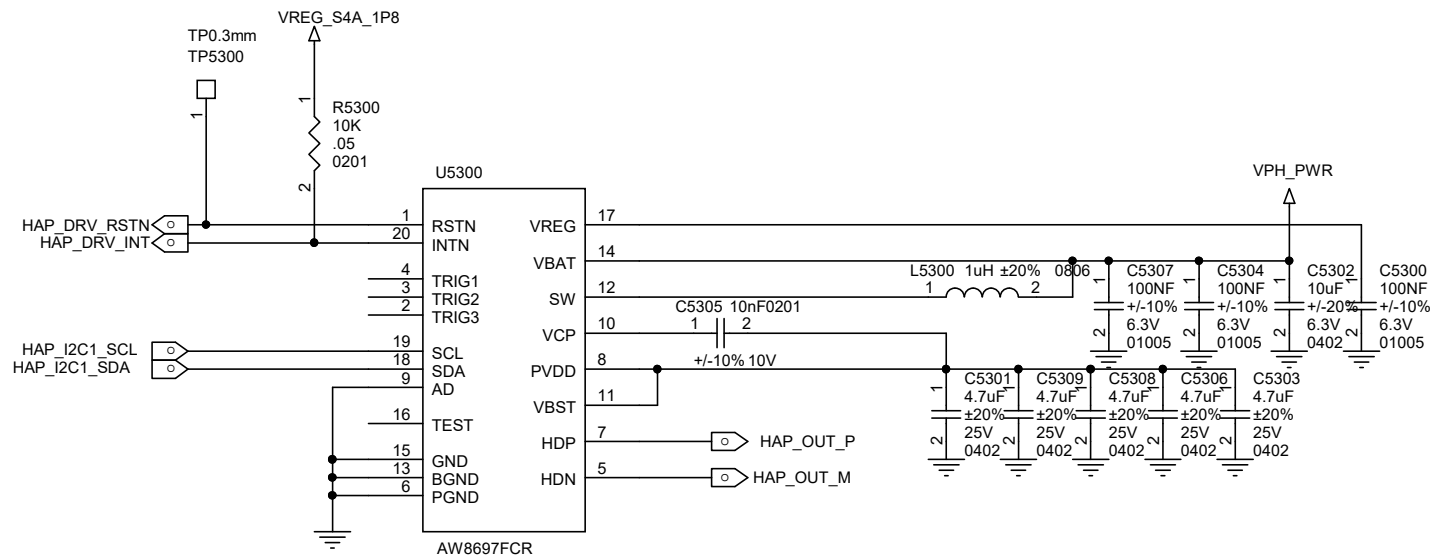
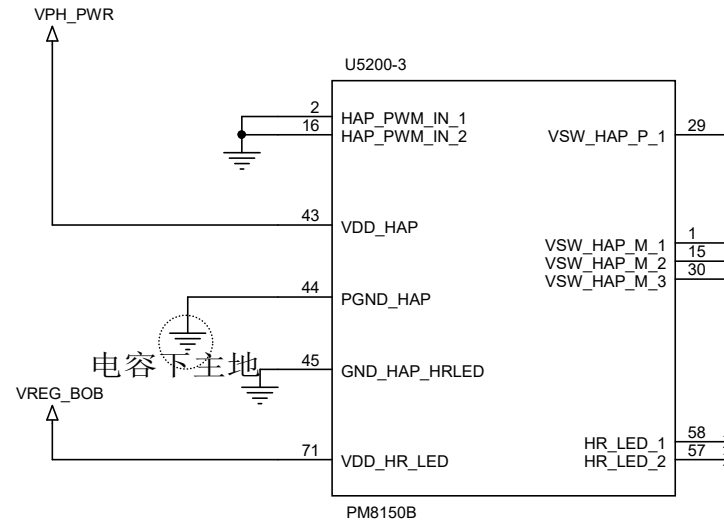



Note: GPIO_11 of PM8150B is for internal usage. Keep it float.

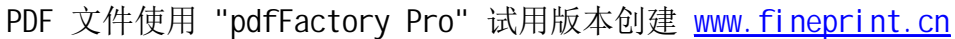
Note: GPIO_04 and GPIO_11 of PM8150B are for internal usage. Keep them float.
Keep GPIO_03 of PM8150B float. Don't use it for other purpose.
GPIO_06 of PM8150B is dedicated for SMB_STAT. Don't use it for other purpose.

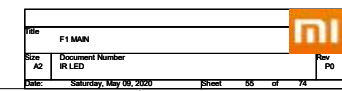
				
Title				
F1 MAIN				
Size	Document Number			Rev
A4	PM855B CTRL&GND			P0
Date:	Saturday, May 09, 2020		Sheet	52 of 74

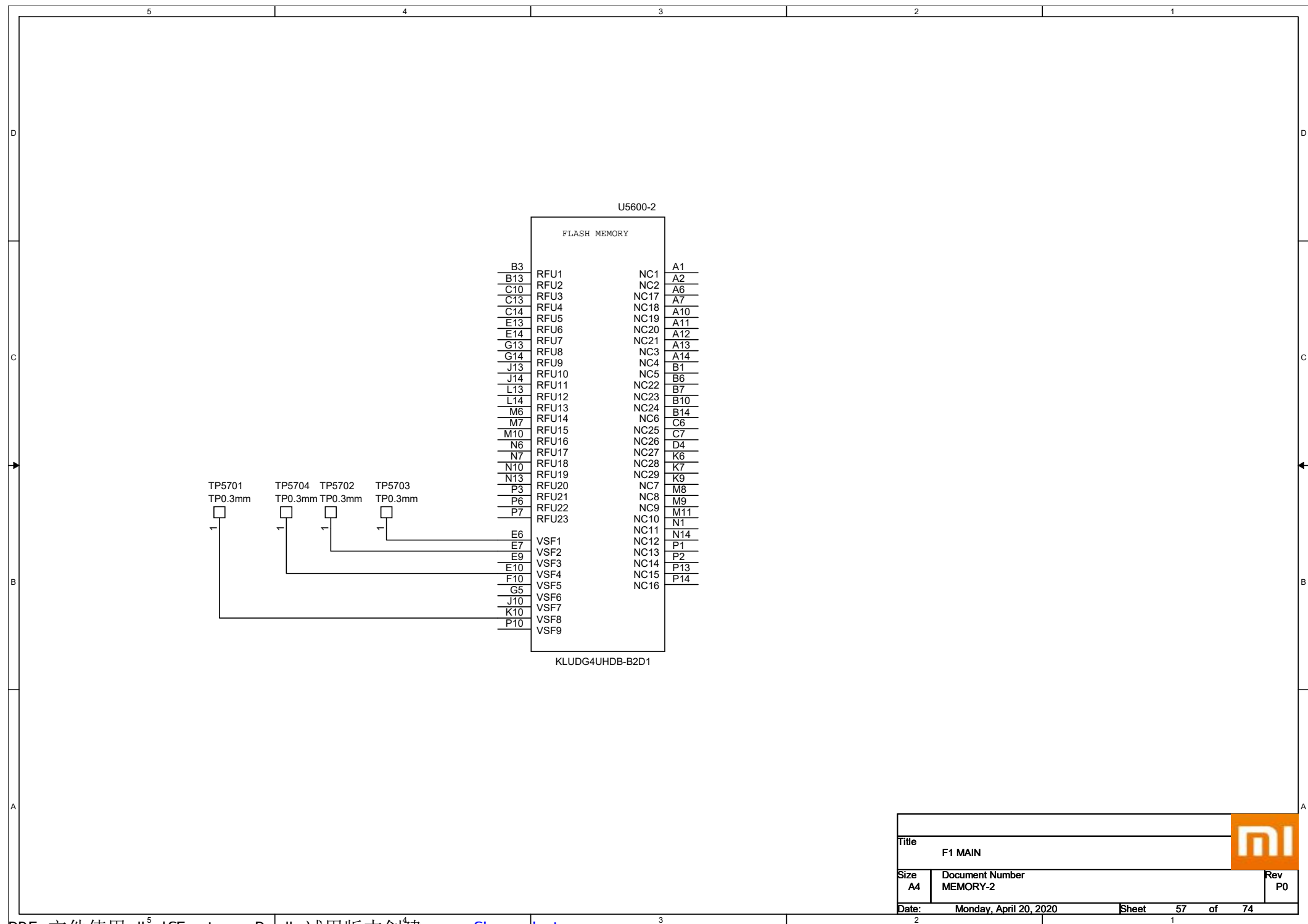
HAPTICS




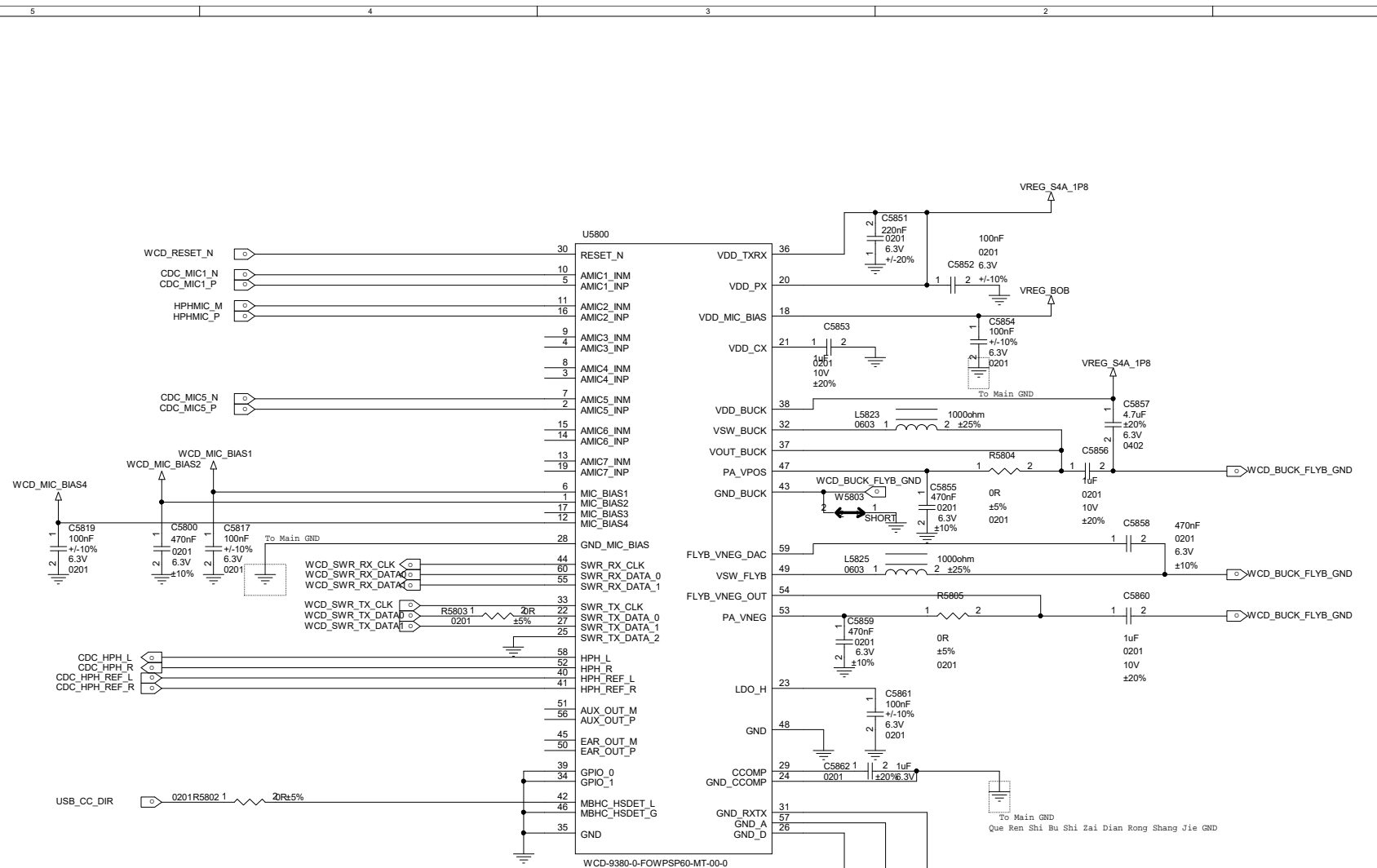
				
Title F1 MAIN				
Size A4	Document Number PM855B HAPTIC			Rev P0
Date:	Saturday, May 09, 2020		Sheet 53 of 74	








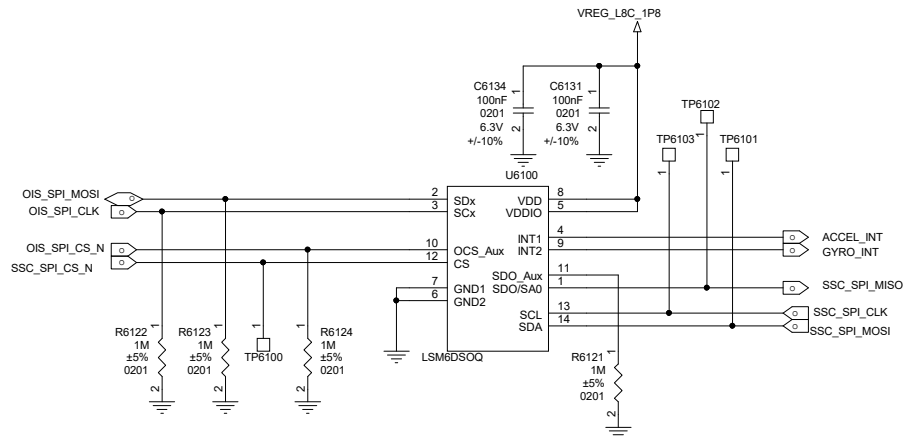
				
Title				
F1 MAIN				
Size	Document Number			Rev
A4	MEMORY-2			P0
Date:	Monday, April 20, 2020		Sheet	57 of 74



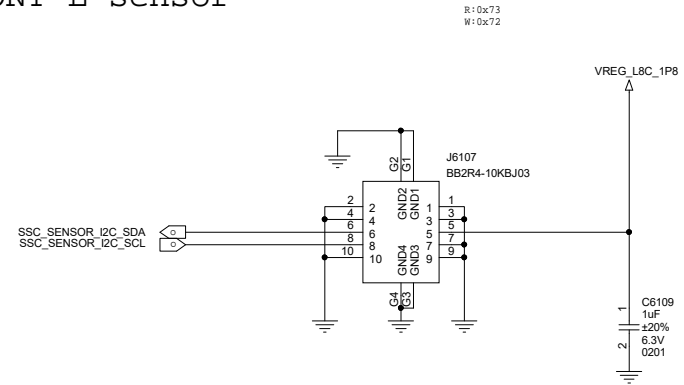
NOTE 1: ROUTE AN ISOLATED GROUND TRACE FROM CCOMP_REF TO THE CAP GROUND, THEN VIA TO THE MAIN GROUND PLANE
 NOTE 2: PLACE VDD_BUCK CAP AS CLOSE TO CHIP EDGE AS POSSIBLE AND MINIMIZE LOOP INDUCTANCE
 NOTE 3: USE SHORT AND WIDE TRACES FOR BUCK AND FLYBACK RELATED NETS
 NOTE 4: ANALOG MIC INPUTS SHOULD BE ISOLATED WITH GROUND FROM NOISY OR SWITCHING SIGNALS AND SUPPLIES

			
Title F1 MAIN			
Size A3	Document Number WCD9340-1	Rev P0	
Date:	Saturday, May 09, 2020	Sheet	58 of 74

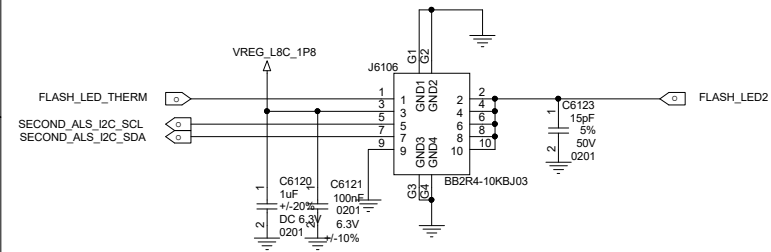
A+G



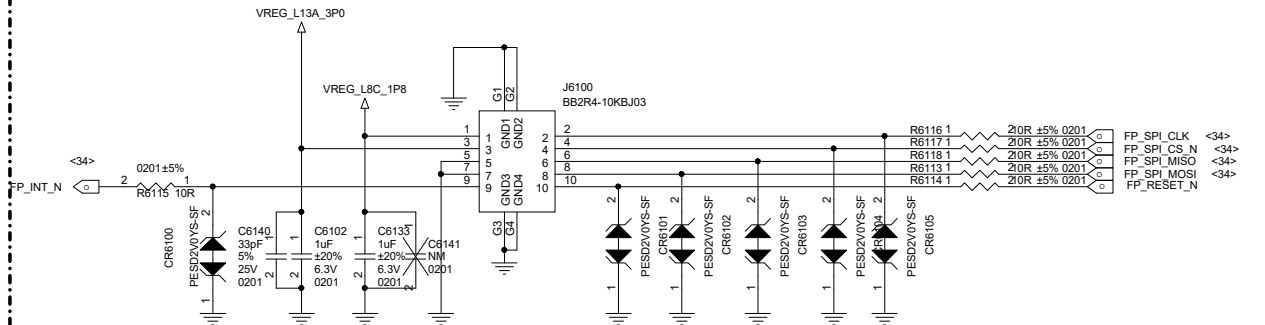
FRONT L-sensor



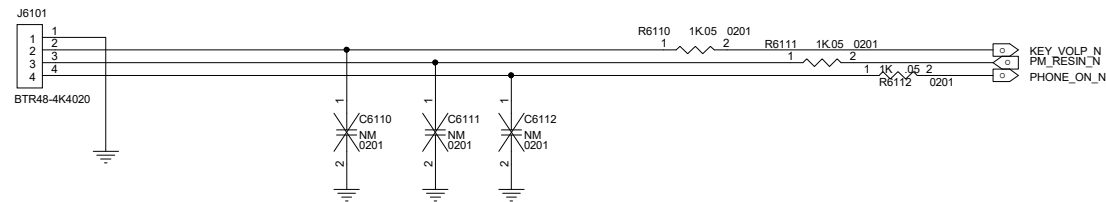
FLASH/BACK L-sensor



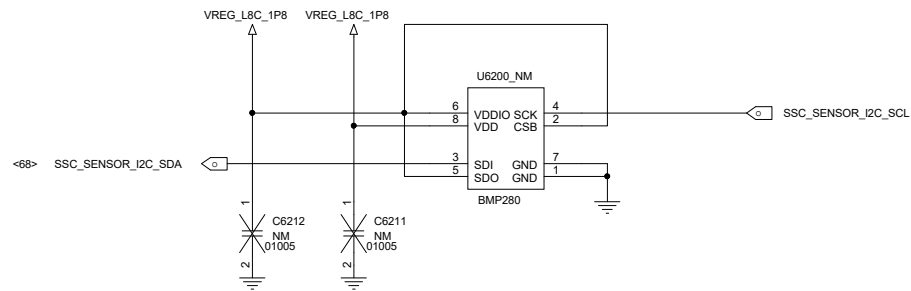
FP



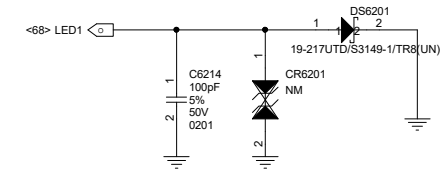
Sidekey



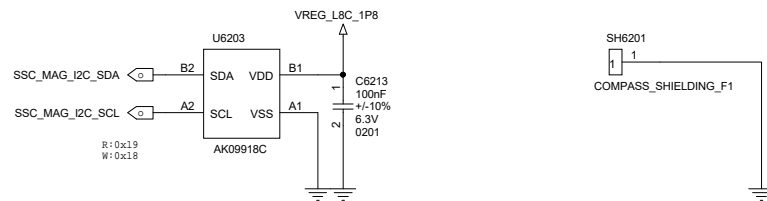
Title		F1 MAIN	Rev P0
Size	Document Number		
A3	SENSOR		
Date	Thursday, May 14, 2020	Sheet	61 of 74



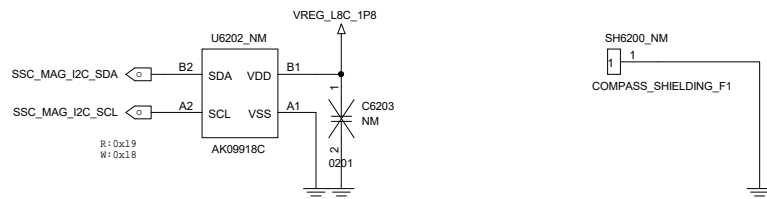
LED



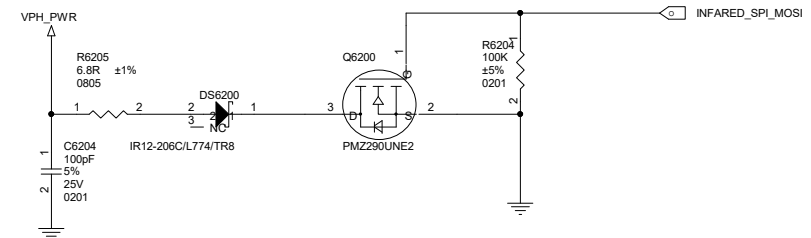
COMPASS



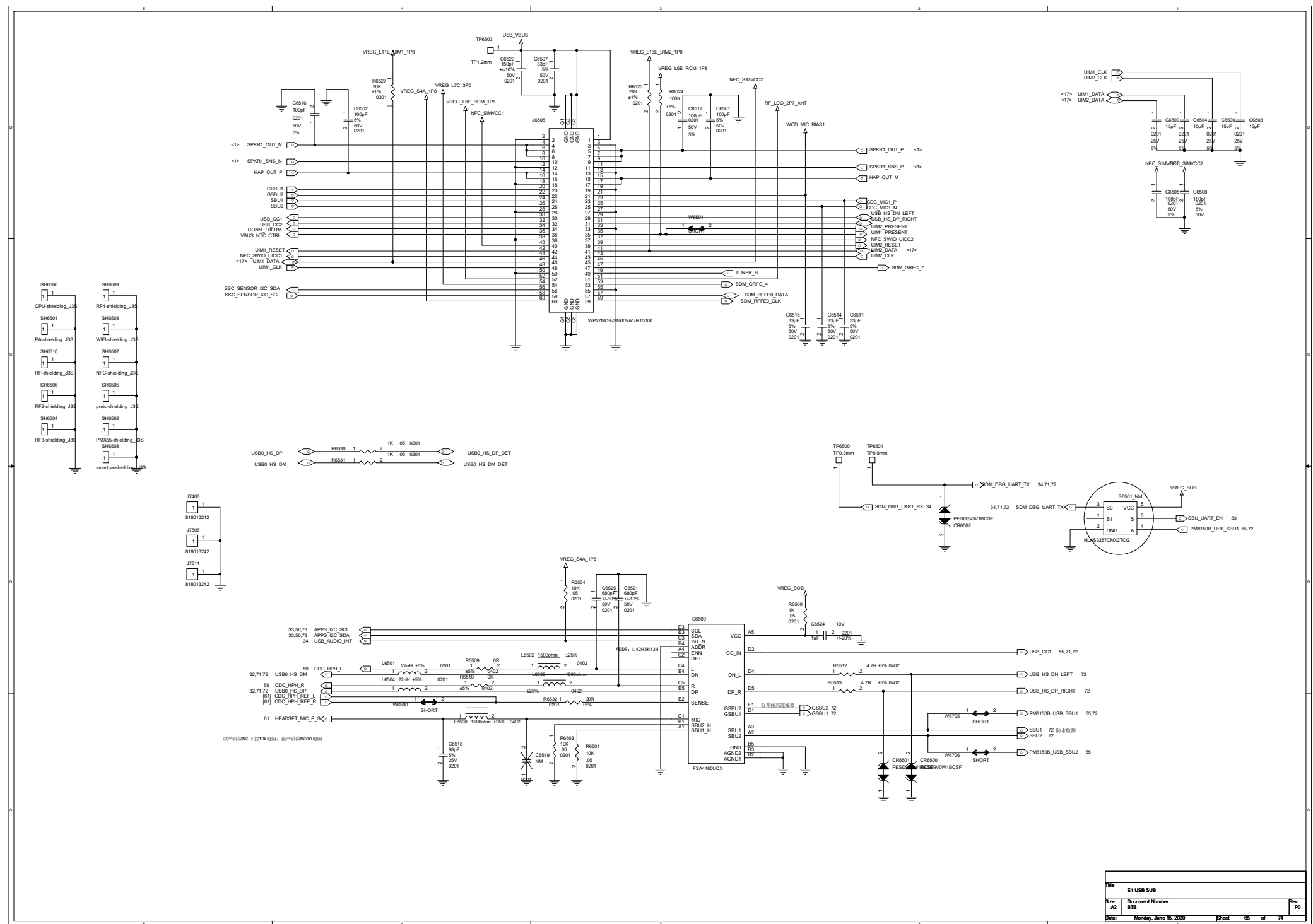
COMPASS

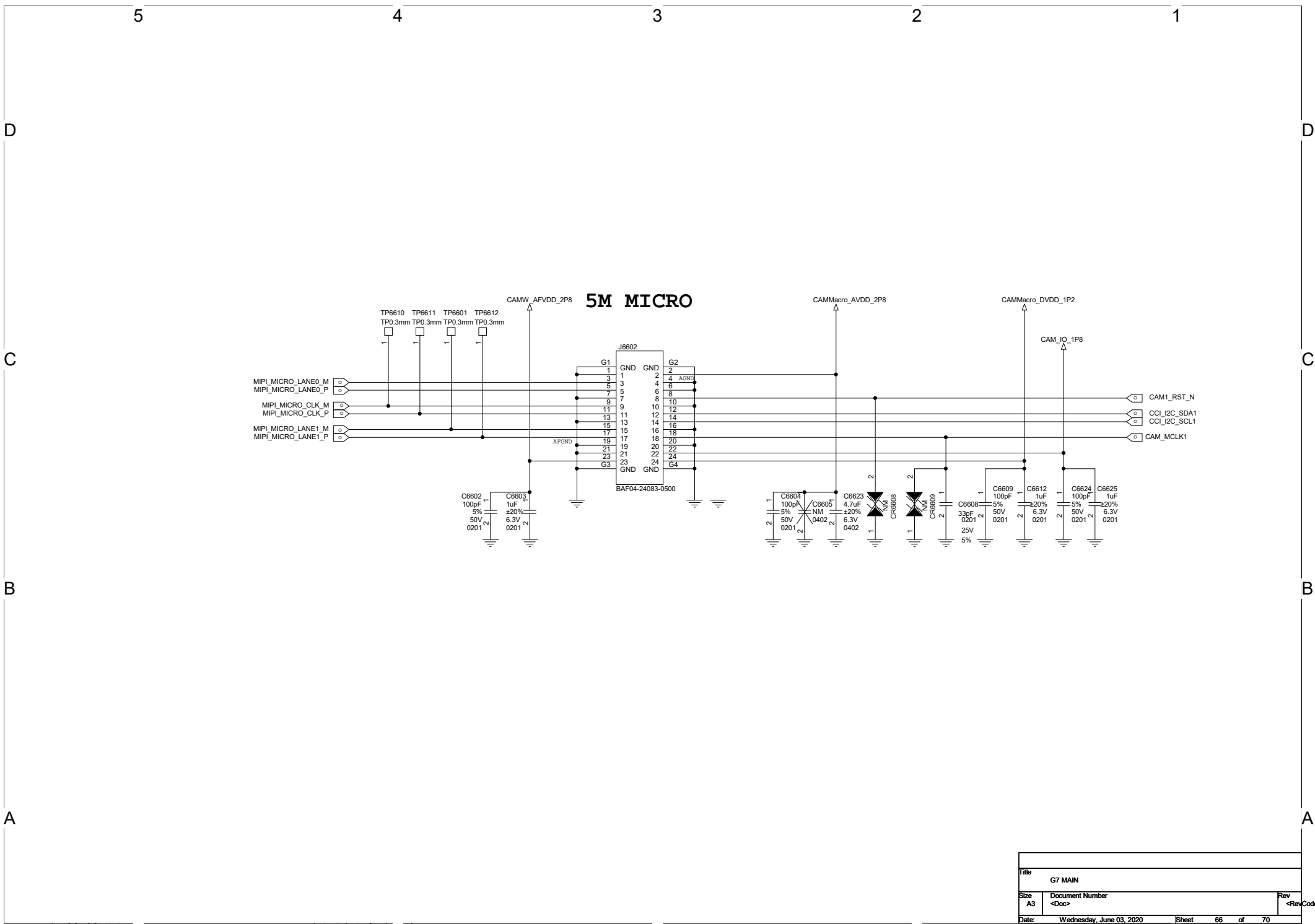


INFRARED



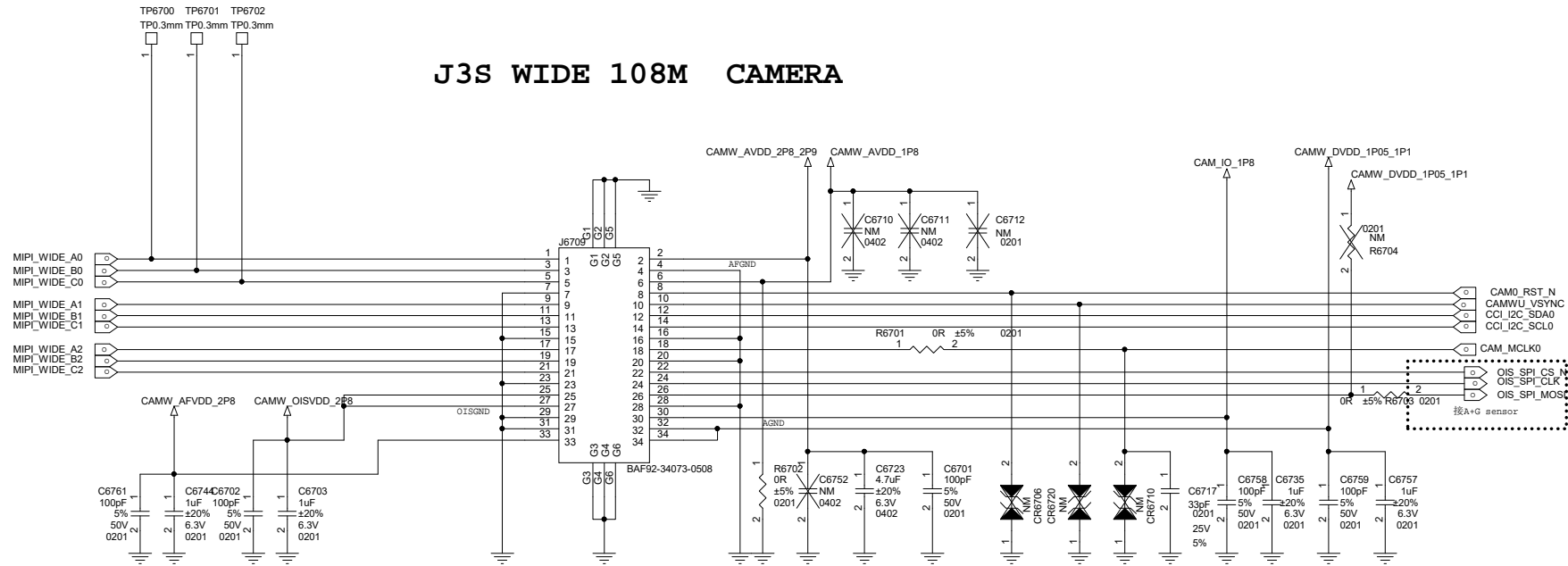
Title		F1 MAIN	
Size	A3	Document Number	SENSOR-2
Date	Wednesday, June 03, 2020	Sheet	62 of 74





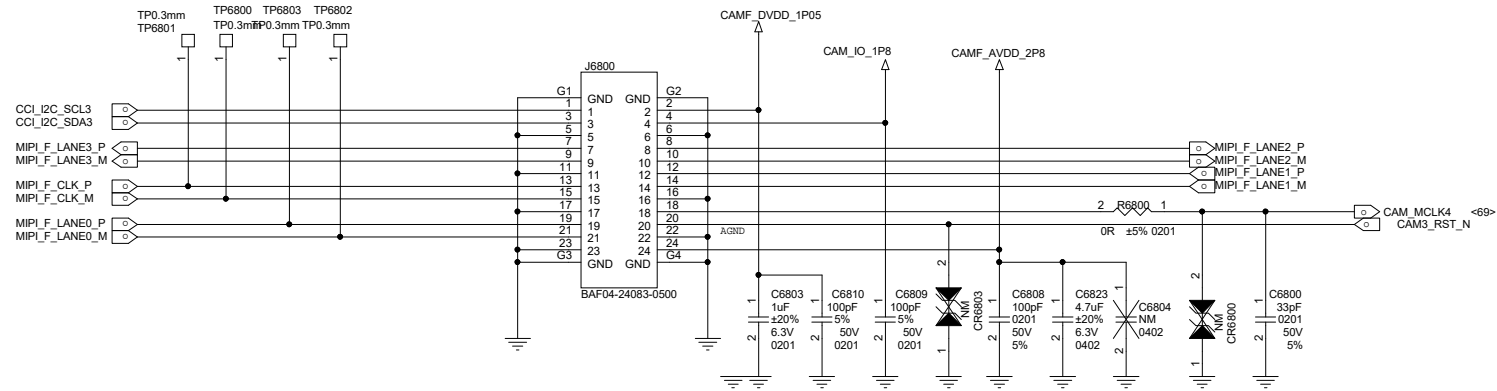
Title		
G7 MAIN		
Size	Document Number	Rev
A3	<Doc>	<RevCode>
Date: Wednesday, June 03, 2020		
Sheet 66 of 70		

J3S WIDE 108M CAMERA



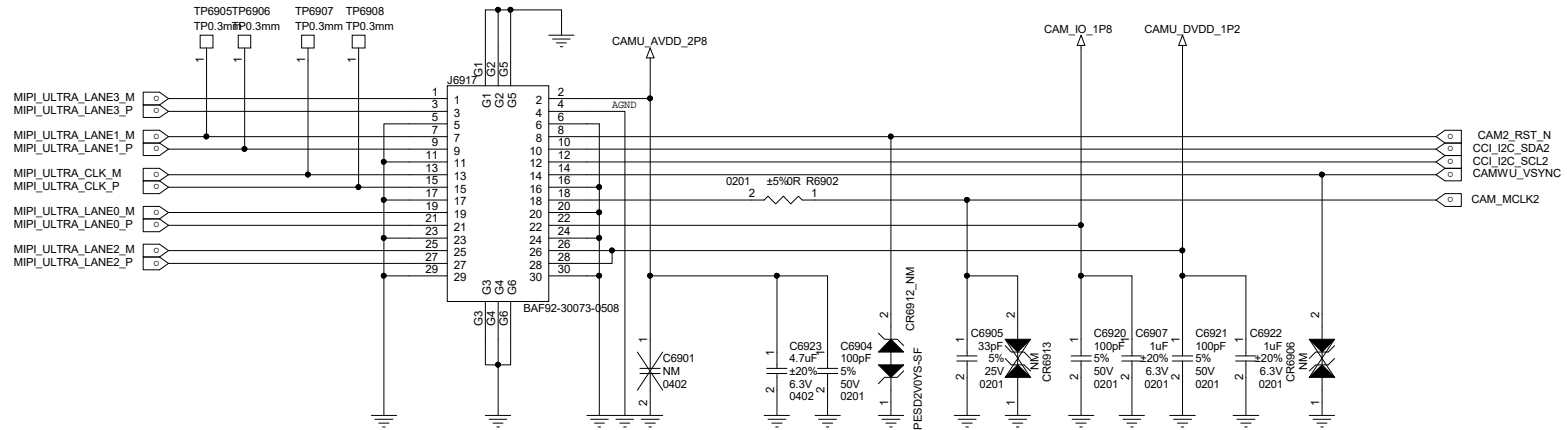
Title		
G7 MAIN		
Size	Document Number	Rev
A3	WIDE&TELE CAMERA	P0
Date:	Saturday, May 09, 2020	Sheet 67 of 70

20M FRONT



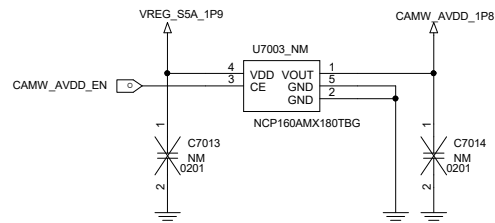
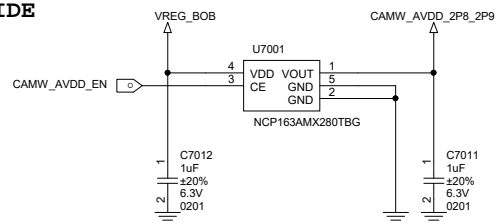
Title		
G7 MAIN		
Size	Document Number	Rev
A3	Reserved(for Analog FM)	P0
Date:	Wednesday, June 03, 2020	Sheet 68 of 70

J3S ULTRA 13M CAMERA



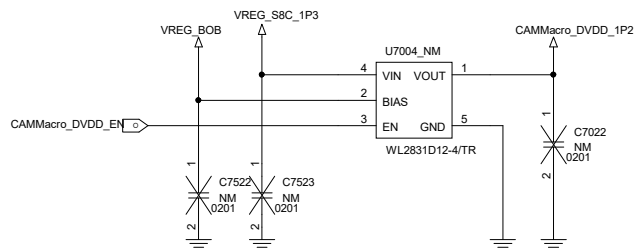
Title		
G7 MAIN		
Size	Document Number	Rev
A3	ULTRA CAMERA	P0
Date	Saturday, May 09, 2020	Sheet 69 of 70

WIDE

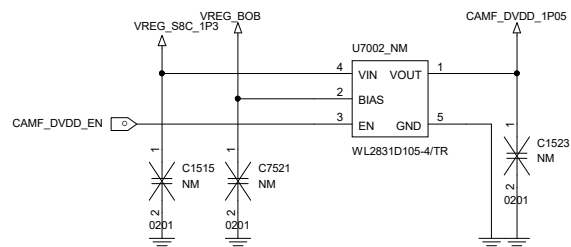


MICRO

此颗WL2831D12-4/TR将其NM掉 作为备选方案



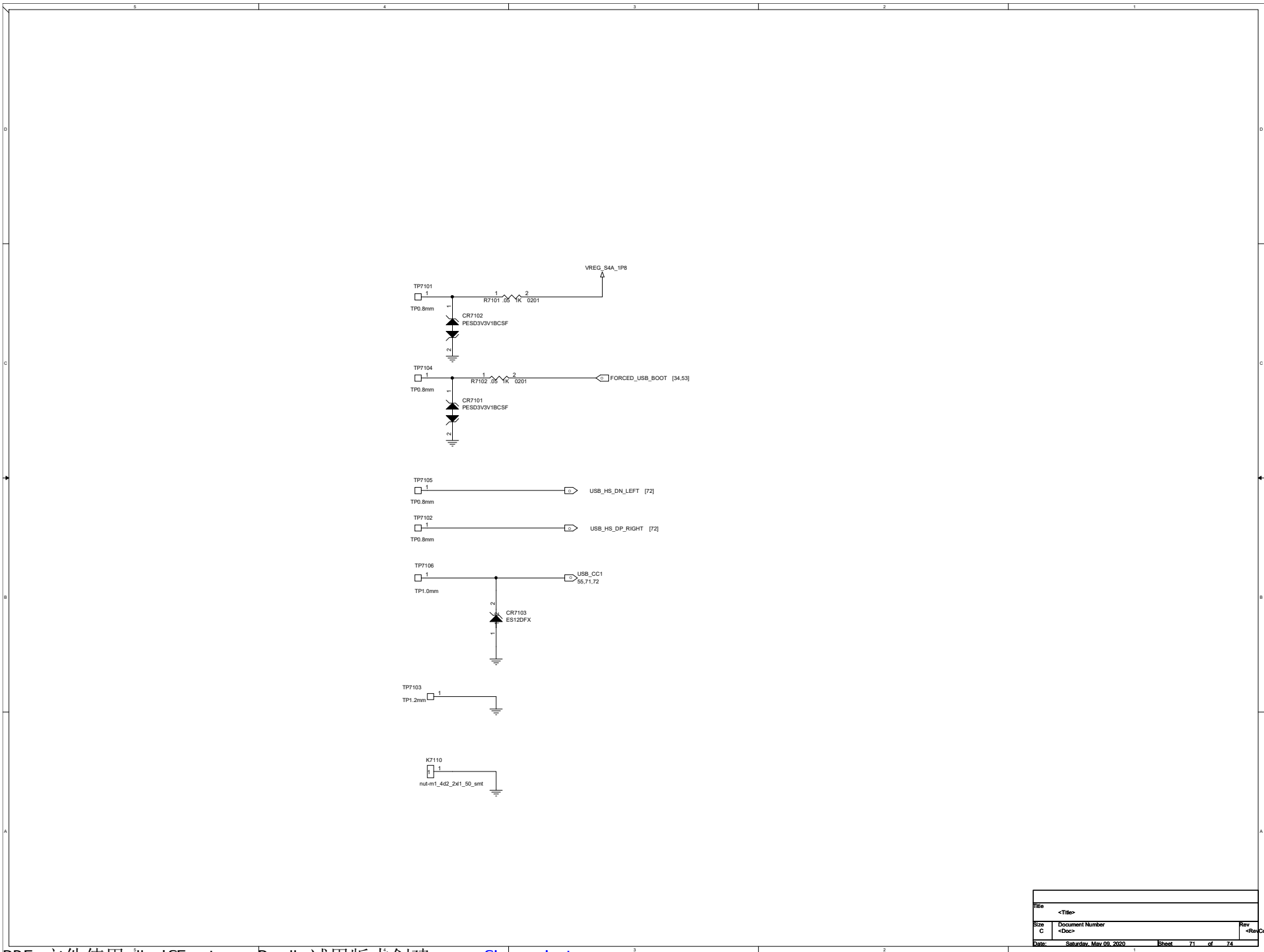
FRONT



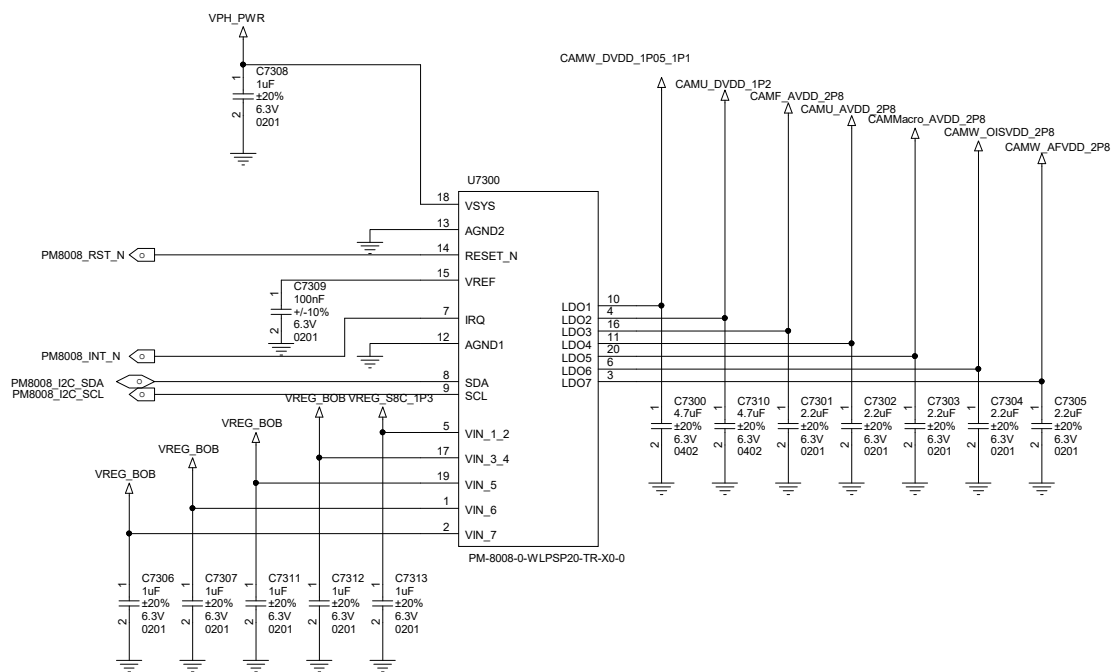
From PMIC power




Title		
G7 MAIN		
Size	Document Number	Rev
A3	CAMERA LDOs	P0
Date	Saturday, May 09, 2020	Sheet 70 of 70



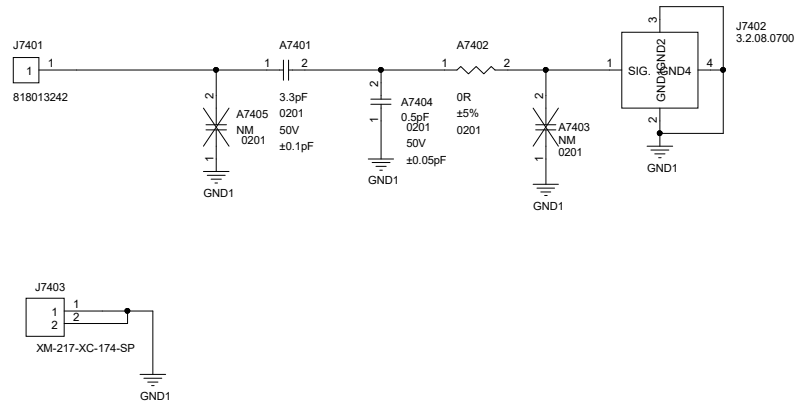
Title		<Title>	
Size	Document Number	Rev	<Rev Code>
C	<Doc>		
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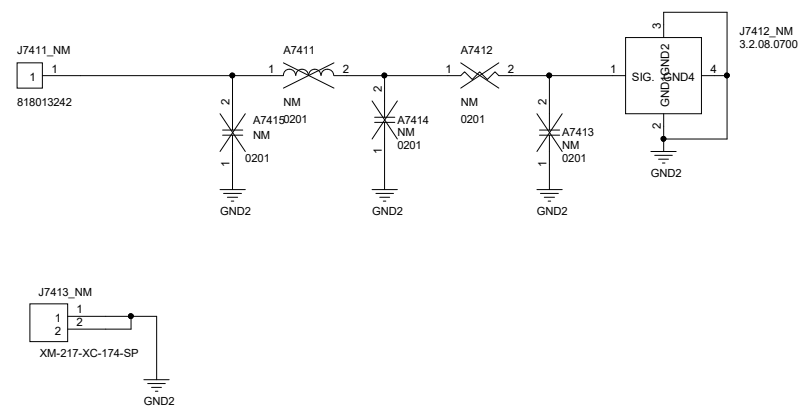
1. C7301为内部参考电压去耦电容，靠近pin 脚放置，
2. I2C address: 0x2,
3. AGND, 打到主地
4. L3~L7 性能相同，可以根据需求配置电压和用途
5. 对于新的sensor可以外加NCP163作为AVDD供电，注意集成LDO和分立LDO的控制时序满足要求

				
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